

RoHS Compliant

USB-Disk Module II Plus

Product Specifications

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Version 1.3

Apacer
Access the best

Apacer Technology Inc.

4th Fl., 75 Hsin Tai Wu Rd., Sec.1, Hsichih, New Taipei City, Taiwan 221
Tel: +886-2-2698-2888 Fax: +886-2-2698-2889
www.apacer.com

FEATURES:

- **USB2.0 High-Speed and USB1.1 Full-Speed compatible interface**
- **Capacity**
 - 4, 8, 16, 32, 64 GB
- **Performance***
 - Sustained read: 33 MB/s
 - Sustained write: 16 MB/s
- **Intelligent USB Module**
 - Implements dynamic wear-leveling algorithms to substantially increase longevity of flash media
 - BCH (24 bit) Error Detection Code/Error Correction Code (EDC/ECC)
 - Intrinsic data integrity after power loss
 - Wear leveling algorithm that provides more reliable data storage over an extended period
- **Extended Data Protection (Optional)**
 - Write protection setting by jumper for prevention of data overwrites.
- **NAND Flash Type: MLC**
- **MTBF**
 - Approximately greater than 3.29×10^6 hours
- **Support Ready Boost for Windows Vista**
- **Support boot-up function for OS**
 - Windows XP Embedded
 - Windows CE
 - Linux
- **Temperature ranges**
 - 0 °C to 70 °C (32 ~ 158 °F) for operation
 - -40 °C to 100 °C (-40 ° ~ 212 °F) for storage
- **Power consumption (typical)***
 - Active mode: 165 mA
 - Sleep mode: 450 μ A
 - Operating voltage: 5V (with a certain percentage of tolerance)
- **Connector**
 - 10-pin (2x5) female header
- **RoHS Compliant**
- **Support Non-Removable Setting**

*The values addressed in Performance and Power consumption are typical and may vary depending on settings and platforms.

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1. General Description

Apacer's USB-Disk Module (UDM) is a high performance, embedded solid state drive (SSD) designed to replace a conventional IDE hard disk drive. UDM SSD can be plugged into a standard Embedded USB connector commonly found in desktops, portable PC systems and industrial PC systems. This product is well suited for embedded flash storage applications offering new and expanded functionality as well as more cost-effective designs, better performance and increased reliability.

This UDM solid state drive is resistant to shock and vibration and compatible with widely used operating systems such as Windows CE, Windows XP Embedded, and Linux. In terms of suitability, it comes in various dimensions to multiple embedded usages.

1.1 Performance-optimized Controller

The heart of an UDM is the USB controller, which translates standard USB signals into the data and controls of the flash media. This proprietary USB controller is specifically designed to attain high data throughput from host to flash.

1.1.1 Power Management Unit (PMU)

The power management unit (PMU) controls the power consumption of the UDM. It can dramatically extend product battery life by leaving the idle part of the circuitry into sleep mode. The PMU has zero wake-up latency.

1.1.2 SRAM Buffer

The SRAM buffer is a key contributor to the USB controller performance. It optimizes data writes to the flash.

1.1.3 Error Correction Code (ECC)

The UDM uses BCH Error Detection Code (EDC) and Error Correction Code (ECC) algorithms which correct up to 24 random single-bit errors for each 1024-byte block of data depending on spare area of flash memory. High performance is achieved through hardware-based error detection and correction.

1.1.4 Wear-Leveling

Flash memory can be erased a limited number of times. This number is called the erase cycle limit, or write endurance limit. The erase cycle limit applies to each individual erase block in the flash device.

In a typical application, and especially if a file system is used, specific pages are constantly updated (e.g., the page that contains the FAT, registry, etc.). Without any special handling, these pages would wear out more rapidly than other pages, reducing the lifetime of the entire flash.

To overcome this inherent deficiency, Apacer's USB-Disk Module (UDM) uses wear-leveling algorithm. This wear-leveling algorithm ensures that consecutive writes of a specific sector are not written physically to the same page in the flash. This spreads flash media usage evenly across all pages, thereby maximizing flash lifetime.

The wear-leveling mechanism provides write/erase cycles for reliable data storage over an extended period.

1.1.5 Write Protection Mode (Optional)

When the device is configured to the Write-Protection mode, it enhances the data security and protection. This feature prevents the stored data from inadvertent system writes or erases, and viruses.

2. Functional Block Diagram

The USB-Disk Module (UDM) contains a controller, embedded firmware, and Flash Media with a female connector. The UDM interfaces with the host system allowing data to be written to and read from the Flash Media.

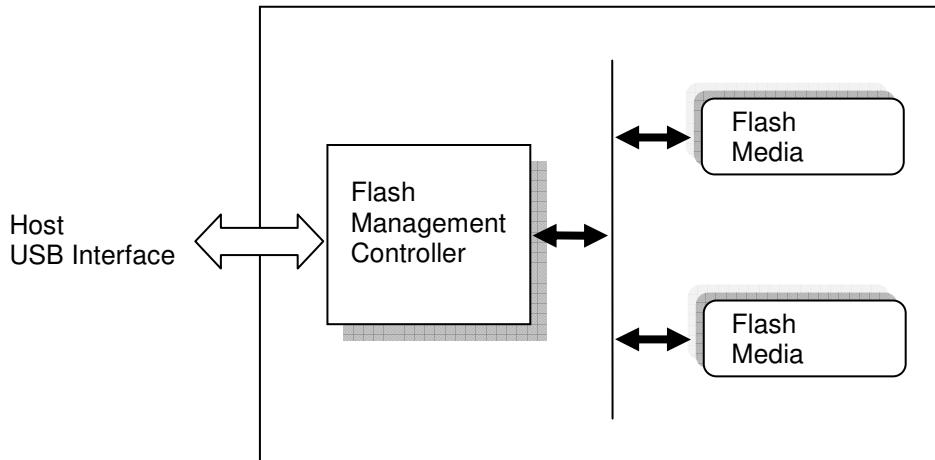


Figure 2-1: Functional block diagram

3. Pin Assignment

This chapter provides the information on the pin assignments and signal descriptions.

3.1 Type A, Type B, and Type C

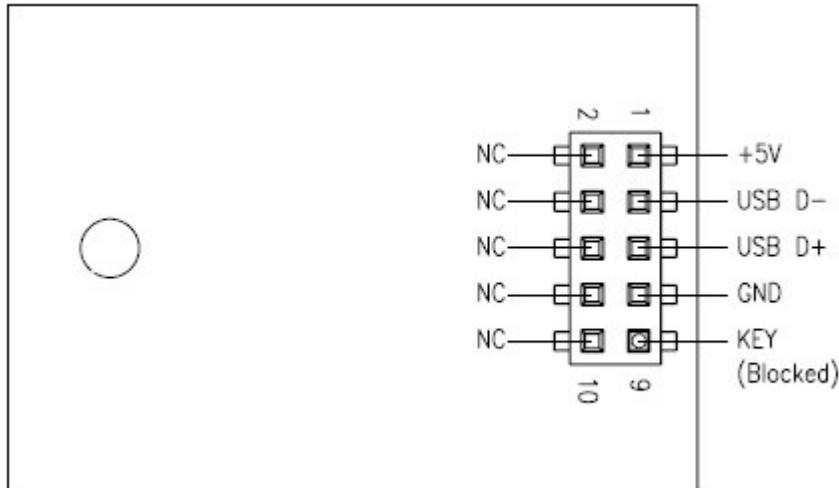


Table 3-1: Pin Assignment of the 10-pin configuration in Type A, Type B, and Type C.

Pin	Signal	Pin	Signal
1	+5V	2	N.C
3	USB D-	4	N.C
5	USB D+	6	N.C
7	GND	8	N.C
9	Key (Blocked)	10	N.C

3.2 Type D (STD)

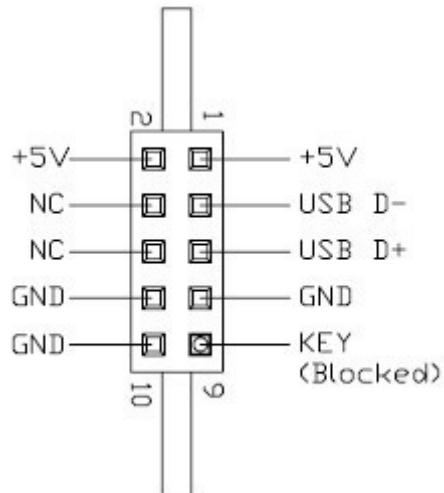


Table 3-2: Pin Assignment of the 10-pin configuration in Type D (STD)

Pin	Signal	Pin	Signal
1	+5V	2	+5V
3	USB D-	4	N.C
5	USB D+	6	N.C
7	GND	8	GND
9	Key (Blocked)	10	GND

4. Performance & Reliability Specifications

4.1 Capacity Specifications

The USB-Disk Module product family is available as below table matrix.

Table 4-1: Capacity Specification

Density	Total Bytes
4GB	4,001,382,400
8GB	8,011,153,408
16GB	16,030,662,656
32GB	32,061,325,312
64GB	64,139,362,304

Note: total user bytes may vary depending on file system in use.

4.2 Performance Specifications

Table 4-2: Performance Specifications

Performance \ Capacity	4 GB	8 GB	16 GB	32 GB	64 GB
Sustained read (MB/s)	32	32	33	33	33
Sustained write (MB/s)	10	10	14	16	16

Note: results may vary depending on flash configurations.

4.3 Environmental Specifications

Environmental specification of the USB-Disk Module (UDM) product family which follows the MIL-STD-810F standards is available as shown in Table 4-3.

Table 4-3: Environmental Specifications

Temperature	Operation 0°C to 70 °C Storage -40°C to 100 °C
Vibration	Sine wave 10 Hz to 500 Hz, 50 m/s ² , 3 axes (IEC 68-2-6, non-operating)
Shock	Half sine wave 50G, 6 axes, 18 times (IEC 68-2-27, non-operating)
EMC	FCC, CE

4.4 MTBF

Mean Time Between Failures (MTBF) is predicted based on reliability data for the individual components in this device. The measurement assumes that device failure rate can be generated by the sum of failure rates in each component and a steady state failure rate can be predicated as equal to or greater than 3.29×10^6 hours.

Notes about the MTBF:

The MTBF is predicated and calculated based on “Bellcore Analysis Method 1”.

$$\lambda_{ss} = \pi_E \sum_{i=1}^m (N_i \lambda_{ssi})$$

m: number of component types

λ_{ss} : device failure rate at steady state

N_i : quantity of *i* type component

λ_{ssi} : failure rate for *i* type component at steady state

π_E : device environment factor

5. Electrical Specification

Caution: Absolute Maximum Stress Ratings – Applied conditions greater than those listed under “Absolute Maximum Stress Ratings” may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these conditions or conditions greater than those defined in the operational sections of this data sheet is not implied. Exposure to absolute maximum stress rating conditions may affect device reliability.

Table 5-1: Operating Range

Range	Ambient Temperature	Voltage
Standard	0°C to 70°C	4.5-5.5V

Table 5-2: Absolute maximum power pin stress ratings

Parameter	Symbol	Min	Max	Unit
Power Supply Input	VDD- VSS	-0.3	5.5	V
Voltage on any pin except VDD with respect to GND	VIN	-ss - 0.3	VDD +0.3	V

5.1 AC Characteristics

Table 5-3: AC Characteristics Full Speed

Parameter	Symbol	Min	Typ	Max	Unit
Rise Time	T _{FR}	4	—	20	ns
Fall Time	T _{FF}	4	—	20	ns
Differential Rise and Fall Time Matching	T _{FRFM}	90	—	111.11	%
Driver Output Resistance	Z _{DRV}	28	—	44	Ω

Table 5-4: AC Characteristics High Speed

Parameter	Symbol	Min	Typ	Max	Unit
Rise Time (10%~90%)	T _{HSR}	500	—	—	ps
Fall Time (10%~90%)	T _{HSF}	500	—	—	ps
Driver Output Resistance	Z _{HSDRV}	40.5	—	49.5	Ω

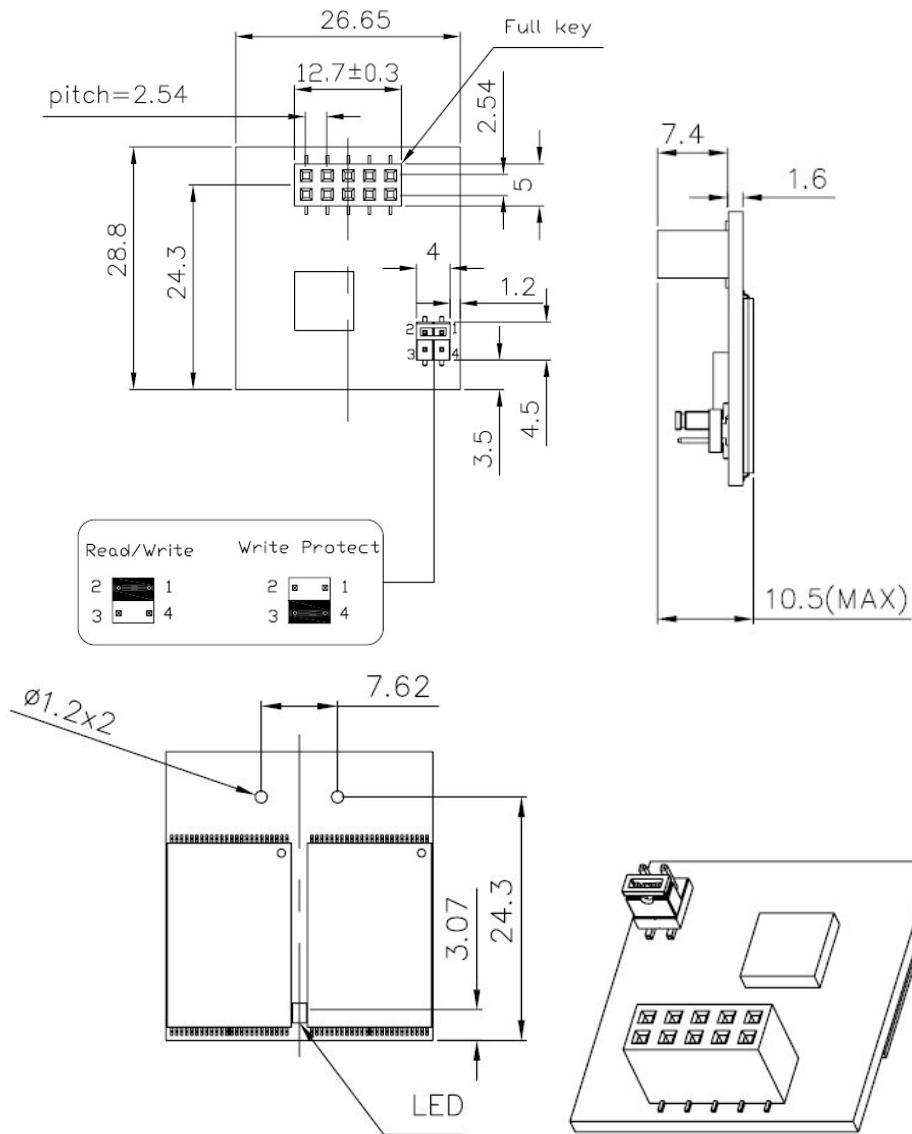
6. Physical Dimension

6.1 Horizontal Design

6.1.1 Type A (STD 90D)

Standard 90 degree

Length	28.8mm	Width	26.65mm	Height (Max)	10.5 mm
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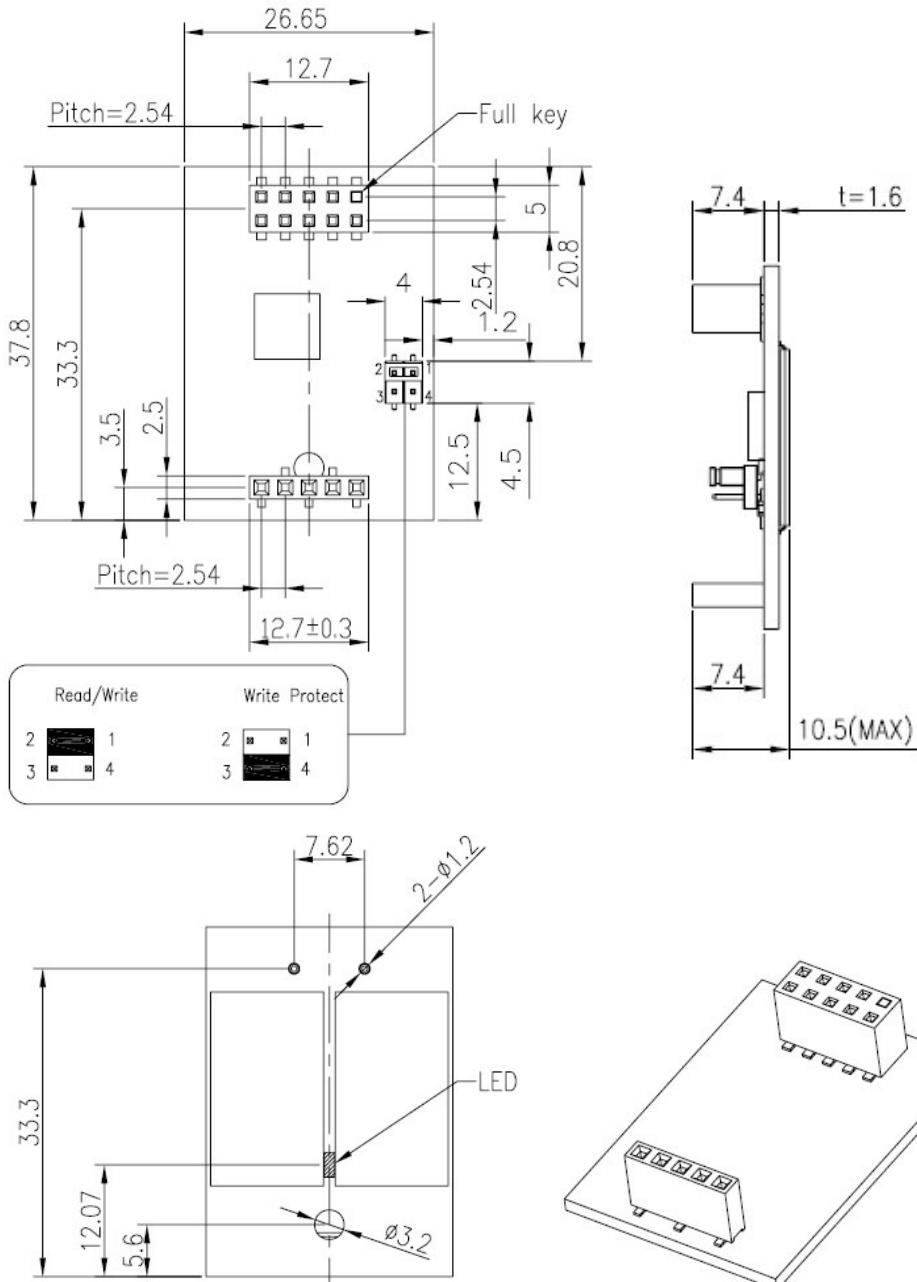


Unit: mm
Tolerance: ± 0.2

6.1.2 Type B (STD 90D-MH)

Standard 90 Degree Mounting Header

Length	37.8mm	Width	26.65mm	Height (Max)	10.5 mm
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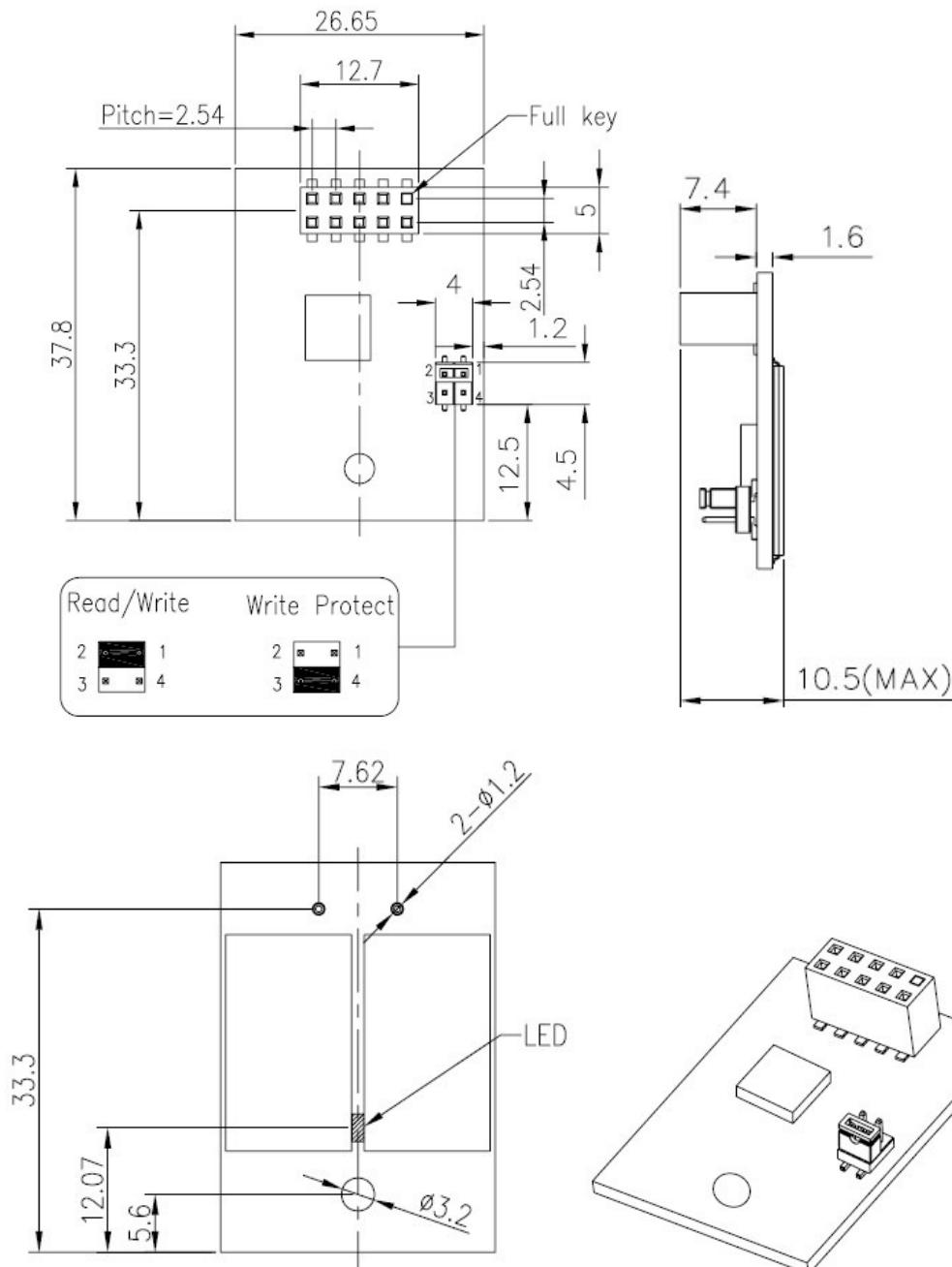


Unit: mm
Tolerance: ± 0.2

6.1.3 Type C (STD 90D-MS)

Standard 90 Degree Mounting Screw

Length	37.8mm	Width	26.65mm	Height (Max)	10.5 mm
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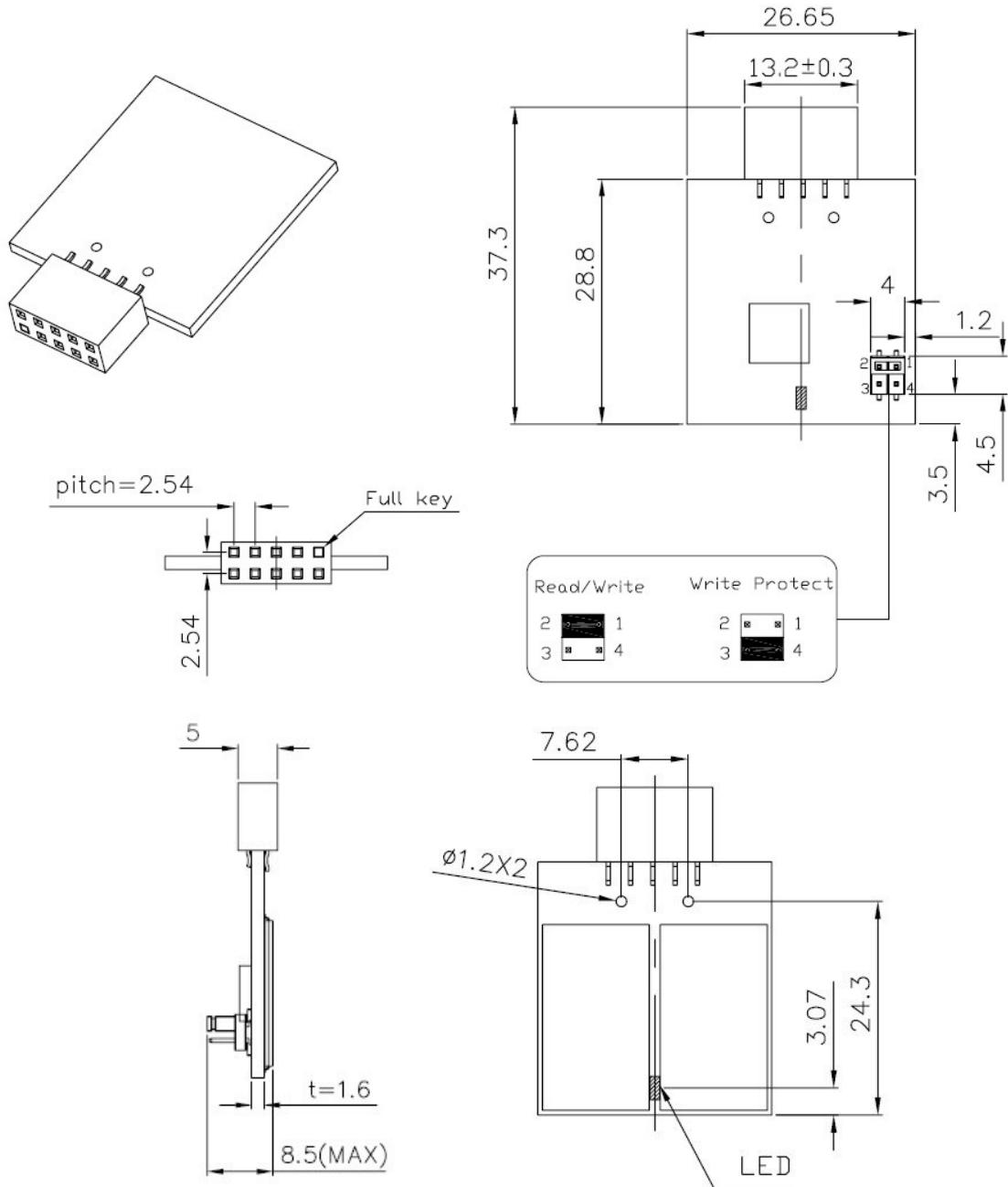
Unit: mm
Tolerance: ± 0.2

6.2 Vertical Design

6.2.1 Type D (STD 180D)

Standard 180 Degree

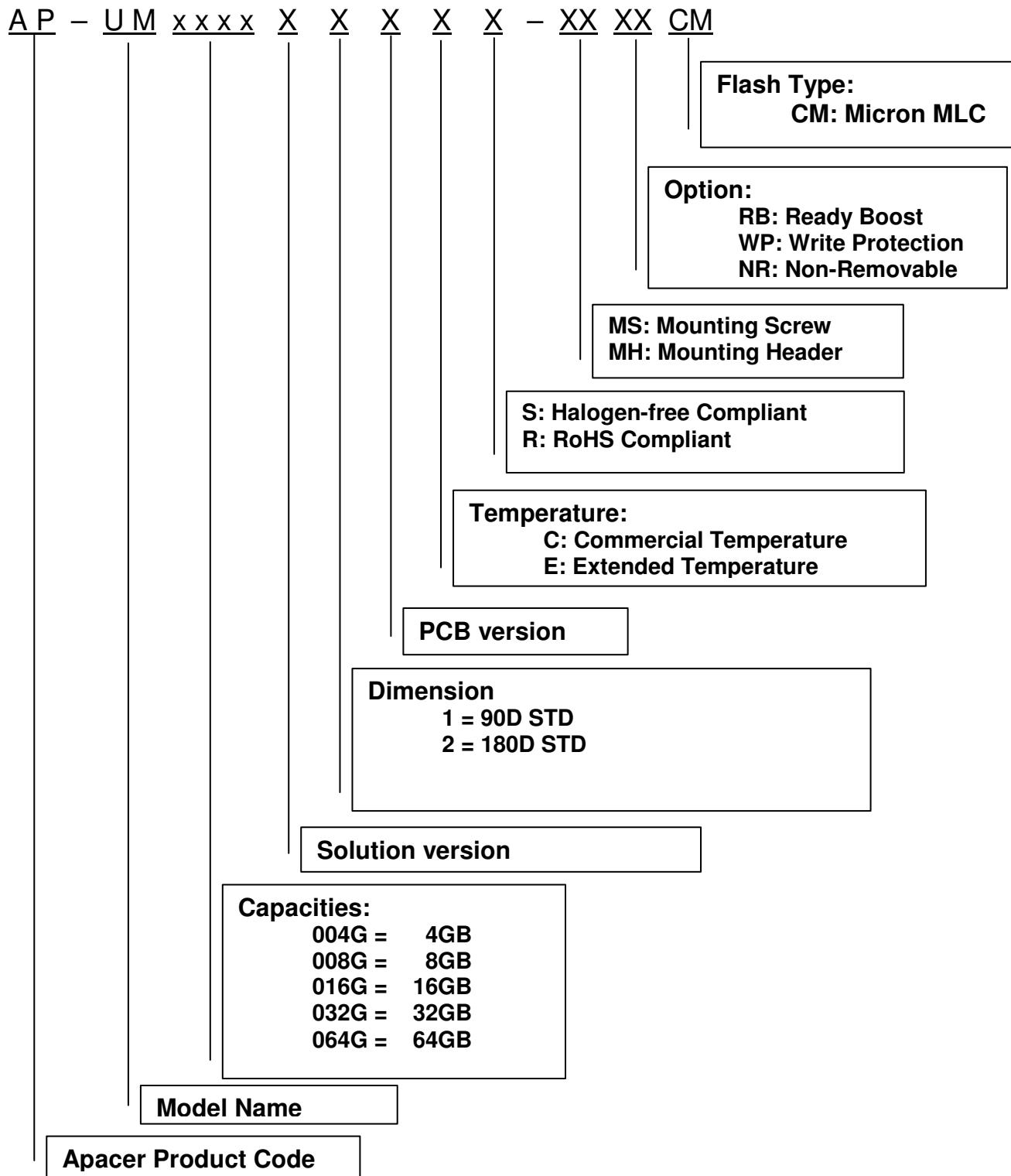
Length	37.3 mm	Width	26.65 mm	Height (Max)	8.5 mm
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Unit: mm
Tolerance: ±0.2

7. Product Ordering Information

7.1 Product Code Designation



7.2 Valid Combinations

7.2.1 Type A (STD 90D)

Standard 90 degree

Capacity	Standard
4GB	AP-UM004GR13CS-CM
8GB	AP-UM008GR13CS-CM
16GB	AP-UM016GR13CS-CM
32GB	AP-UM032GR13CS-CM
64GB	AP-UM064GR13CS-CM

7.2.2 Type B (STD 90D-MH)

Standard 90 degree Mounting Header

Capacity	Standard
4GB	AP-UM004GR13CS-MHCM
8GB	AP-UM008GR13CS-MHCM
16GB	AP-UM016GR13CS-MHCM
32GB	AP-UM032GR13CS-MHCM
64GB	AP-UM064GR13CS-MHCM

7.2.3 Type C (STD 90D-MS)

Standard 90 degree Mounting Screw

Capacity	Standard
4GB	AP-UM004GR13CS-MSCM
8GB	AP-UM008GR13CS-MSCM
16GB	AP-UM016GR13CS-MSCM
32GB	AP-UM032GR13CS-MSCM
64GB	AP-UM064GR13CS-MSCM

7.2.4 Type D (STD 180D)

Standard 180 degree

Capacity	Standard
4GB	AP-UM004GR23CS-CM
8GB	AP-UM008GR23CS-CM
16GB	AP-UM016GR23CS-CM
32GB	AP-UM032GR23CS-CM
64GB	AP-UM064GR23CS-CM

Note: Valid combinations are those products in mass production or will be in mass production. Consult your Apacer sales representative to confirm availability of valid combinations and to determine availability of new combinations.

Revision History

Revision	Description	Date
1.0	Official Release	December 13, 2011
1.1	Added MTBF section	May 10 th , 2012
1.2	Updated power consumption	June 29 th , 2012
1.3	Updated contact phone number of Apacer office in America	August 30 th , 2012

Global Presence

Taiwan (Headquarters)

Apacer Technology Inc.
4th Fl., 75 Hsin Tai Wu., Sec.1
Hsichih, New Taipei City
Taiwan 221
R.O.C.
Tel: +886-2-2698-2888
Fax: +886-2-2698-2889
amtsales@apacer.com

U.S.A.

Apacer Memory America, Inc.
386 Fairview Way, Suite102,
Milpitas, CA 95035
Tel: 1-408-518-8699
Fax: 1-408-935-9611
sa@apacerus.com

Japan

Apacer Technology Corp.
5F, Matsura Bldg., Shiba, Minato-Ku
Tokyo, 105-0014, Japan
Tel: 81-3-5419-2668
Fax: 81-3-5419-0018
jpservices@apacer.com

Europe

Apacer Technology B.V.
Science Park Eindhoven 5051 5692 EB Son,
The Netherlands
Tel: 31-40-267-0000
Fax: 31-40-267-0000#6199
sales@apacer.nl

China

Apacer Electronic (Shanghai) Co., Ltd
1301, No.251,Xiaomuqiao Road, Shanghai,
200032, China
Tel: 86-21-5529-0222
Fax: 86-21-5206-6939
sales@apacer.com.cn

India

Apacer Technologies Pvt. Ltd.
#143, 1st Floor, Raheja Arcade,
5th Block Kormangala Industrial Layout,
Bangalore - 560095, India
Tel: 91-80-4152-9061
sales_india@apacer.com