



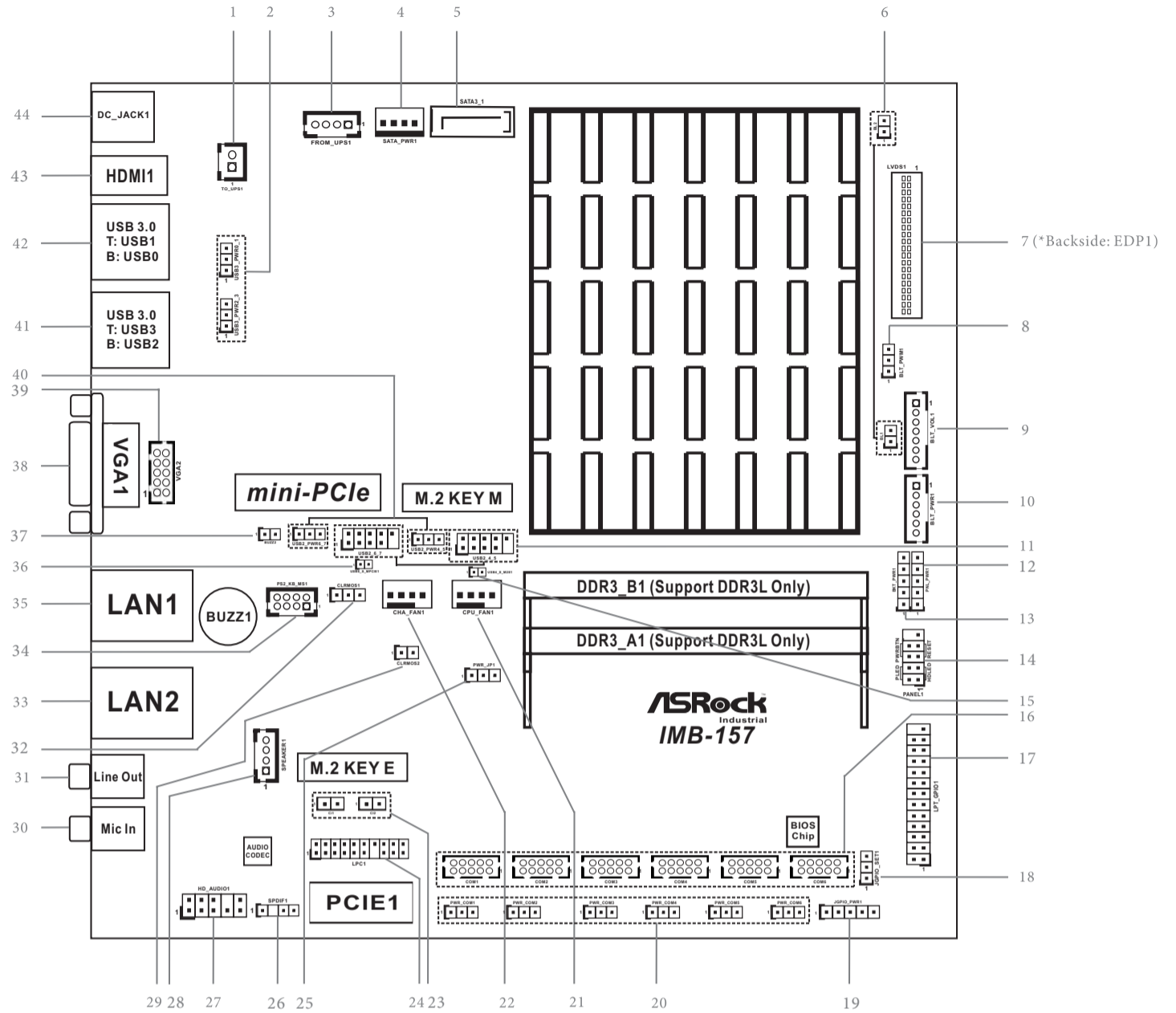
P/N: 15G06M052000AK V1.0



Jumpers and Headers Setting Guide

IMB-157

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1 : 2-pin UPS Module Power Input Connector

Pin1: GND
Pin2: DC Input



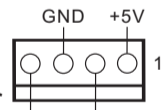
2 : USB Power Setting Jumpers

(USB3_PWR0_1 (For USB3_0_1))
(USB3_PWR2_3 (For USB3_2_3))
1-2: +5V
2-3: +5VSB



3 : UPS Module Power Output Connector

(Input +12V & +19V~+24V)
Pin1 and Pin4: GND
Pin2 and Pin3: DC Input



4 : SATA Power Output Connector



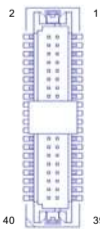
5 : SATA3 Connector (SATA3_1)



6 : BL1, BL2

7 : LVDS Panel Connector

PIN	Signal Name	PIN	Signal Name
2	LCD_VCC	1	LCD_VCC
4	LDDC_CLK	3	+3.3V
6	LVDS_A_DATA0#	5	LDDC_DATA
8	GND	7	LVDS_A_DATA0
10	LVDS_A_DATA1	9	LVDS_A_DATA1#
12	LVDS_A_DATA2#	11	GND
14	GND	13	LVDS_A_DATA2
16	LVDS_A_DATA3	15	LVDS_A_DATA3#
18	LVDS_A_CLK#	17	GND
20	GND	19	LVDS_A_CLK
22	LVDS_B_DATA0	21	LVDS_B_DATA0#
24	LVDS_B_DATA1#	23	GND
26	GND	25	LVDS_B_DATA1
28	LVDS_B_DATA2	27	LVDS_B_DATA2#
30	LVDS_B_DATA3#	29	DPLVDD_EN
32	GND	31	LVDS_B_DATA3
34	LVDS_B_CLK	33	LVDS_B_CLK#
36	CON_LBKLT_EN	35	GND
38	LCD_BLT_VCC	37	CON_LBKLT_CTL
40	LCD_BLT_VCC	39	LCD_BLT_VCC



* eDP Connector
(on the Backside of PCB)

8 : Backlight Control Level (BLT_PWM1)

1-2: From eDP PWM to CON_LBKLT_CTL
2-3: From LVDS PWM to CON_LBKLT_CTL



9 : Backlight Volume Control (BLT_VOL1)

PIN	Signal Name
1	GPIO_VOL_UP
2	GPIO_VOL_DW
3	PWRDN
4	BRIGHTNESS_UP
5	BRIGHTNESS_DW
6	GND
7	GND

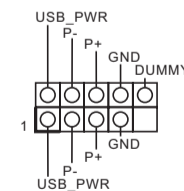


10 : Backlight Power Connector (BLT_PWR1)

PIN	Signal Name
1	GND
2	GND
3	CON_LBKLT_CTL
4	CON_LBKLT_EN
5	LCD_BLT_VCC
6	LCD_BLT_VCC



11 : USB2.0 Connectors
(USB2_4_5, USB2_6_7)



12 : Panel Power Select (LCD_VCC)
(PNL_PWR1)

1-2: LCD_VCC: +3V
2-3: LCD_VCC: +5V
4-5: LCD_VCC: +12V

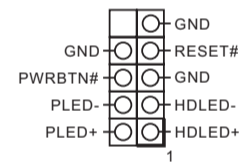


13 : Backlight Power Select
(LCD_BLT_VCC) (BKT_PWR1)

1-2: LCD_BLT_VCC: +5V
2-3: LCD_BLT_VCC: +12V
4-5: LCD_BLT_VCC: DC Input



14 : System Panel Header

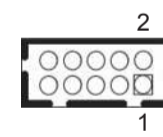


15 : USB4_S_M2E1

Pin1 short USB2_4_5 pin3 and pin2
short USB2_4_5 pin5: share USB port 4
of USB2_4_5 header to M.2 KEY E

16 : Internal COM Port Headers

COM1, 2 (RS232/422/485)
COM3, 4, 5, 6 (RS232)

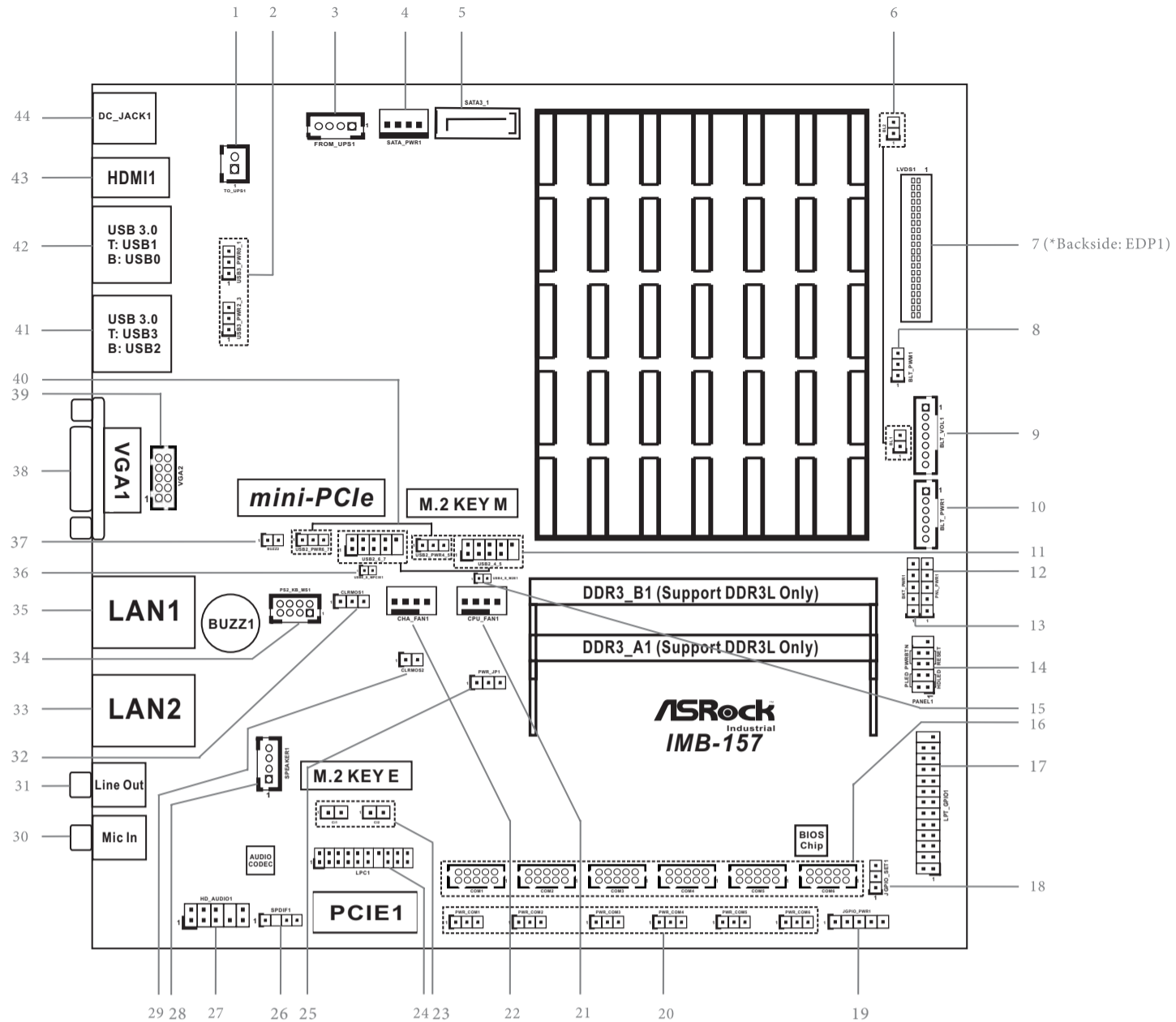


PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
10	N/A	8	CCTS#	6	DDSR#	4	DDTR#	2	RRXD
9	PWR	7	RRTS#	5	GND	3	TTXD	1	DDCD#

* This motherboard supports RS232/422/485 on COM1, 2 ports. Please refer to below table for the pin definition. In addition, COM1, 2 ports (RS232/422/485) can be adjusted in BIOS setup utility > Advanced Screen > Super IO Configuration. You may refer to our user manual for details.

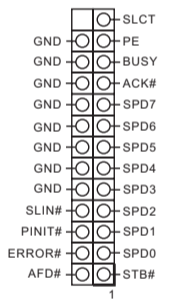
COM1, 2 Port Pin Definition

PIN	RS232	RS422	RS485
1	DCD	TX-	RTX-
2	RXD	RX+	N/A
3	TXD	TX+	RTX+
4	DTR	RX-	N/A
5	GND	GND	GND
6	DSR	N/A	N/A
7	RTS	N/A	N/A
8	CTS	N/A	N/A
9	PWR	PWR	PWR
10	N/A	N/A	N/A



17 : Printer Port / GPIO Header (LPT_GPIO1)

Printer Port:

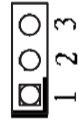


GPIO:

PIN	Signal Name	PIN	Signal Name
26	NC	25	NA
24	GND	23	SIO_GP30
22	GND	21	SIO_GP31
20	GND	19	SIO_GP32
18	GND	17	SIO_GP33
16	GND	15	SIO_GP34
14	GND	13	SIO_GP35
12	JGPIO_PWR	11	SIO_GP36
10	JGPIO_PWR	9	SIO_GP37
8	SIO_GP43	7	SIO_GP40
6	SIO_GP44	5	SIO_GP41
4	SIO_GP45	3	SIO_GP42
2	SIO_GP46	1	SIO_GP47

* If you want to use the printer port function, please short pin4 and pin5 on Digital Input / Output Power Select (JGPIO_PWR1).

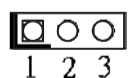
18 : Digital Input / Output Default Value Setting (JGPIO_SET1)
 1-2: Pull-High
 2-3: Pull-Low



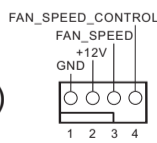
19 : Digital Input / Output Power Select (JGPIO_PWR) (JGPIO_PWR1)
 1-2: +12V
 2-3: +5V
 4-5: GND



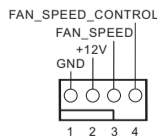
20 : COM Port Pin9 PWR Setting Jumpers PWR_COM1 ~ 6 (For COM Port1 ~ 6)
 1-2: +5V
 2-3: +12V



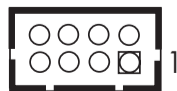
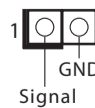
21 : 4-Pin CPU FAN Connector (+12V)



22 : 4-Pin Chassis FAN Connector (+12V)

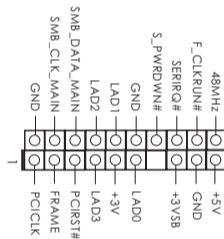


23 : Chassis Intrusion Headers
 C11:
 Close: Active Case Open
 Open: Normal
 C12:
 Close: Normal
 Open: Active Case Open

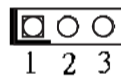


PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
8	GND	6	GND	4	USB2_PWR6_7	2	USB2_PWR6_7
7	MSCLK	5	MSDATA	3	KBDATA	1	KBCLK

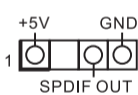
24 : LPC Header



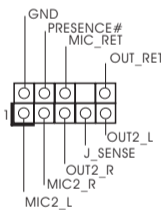
25 : ATX/AT Mode Select
 1-2: AT Mode
 2-3: ATX Mode



26 : SPDIF Header

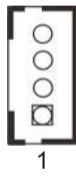


27 : Front Panel Audio Header



28 : 3W Audio AMP Output Wafer

PIN	Signal Name
4	OUTRN
3	OUTRP
2	OUTLP
1	OUTLN

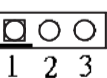


29 : Clear CMOS Jumper
 Open: Normal
 Short: Auto Clear CMOS When AC Power On



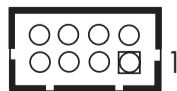
30 : Audio Output: Pink – Mic In
 31 : Audio Output: Green – Line Out

32 : Clear CMOS Header
 1-2: Normal
 2-3: Clear CMOS



33 : RJ45 LAN Port (LAN2)

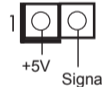
34 : PS2 Keyboard/Mouse Header



35 : RJ45 LAN Port (LAN1)

36 : USB6_S_MPCIE1
 Pin1 short USB2_6_7 pin3 and pin2 short USB2_6_7 pin5: share USB port 6 of USB2_6_7 header to MPCIE1

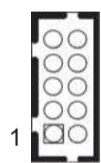
37 : Buzzer
 +5V Signal



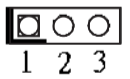
38 : VGA Port

39 : VGA Header

PIN	Signal Name	PIN	Signal Name
9	DDC_CLK	10	DDC_DATA
7	HSYNC	8	VSYNC
5	BLUE	6	GND
3	GREEN	4	GND
1	RED	2	GND



40 : USB Power Setting Jumpers (USB2_PWR4_5 (For USB2_4_5)) (USB2_PWR6_7 (For USB2_6_7 and PS2_KB_MS1))
 1-2: +5V
 2-3: +5VSB



41 : USB3.0 Ports (USB3_2_3)
 42 : USB3.0 Ports (USB3_0_1)
 43 : HDMI Port
 44 : DC Jack