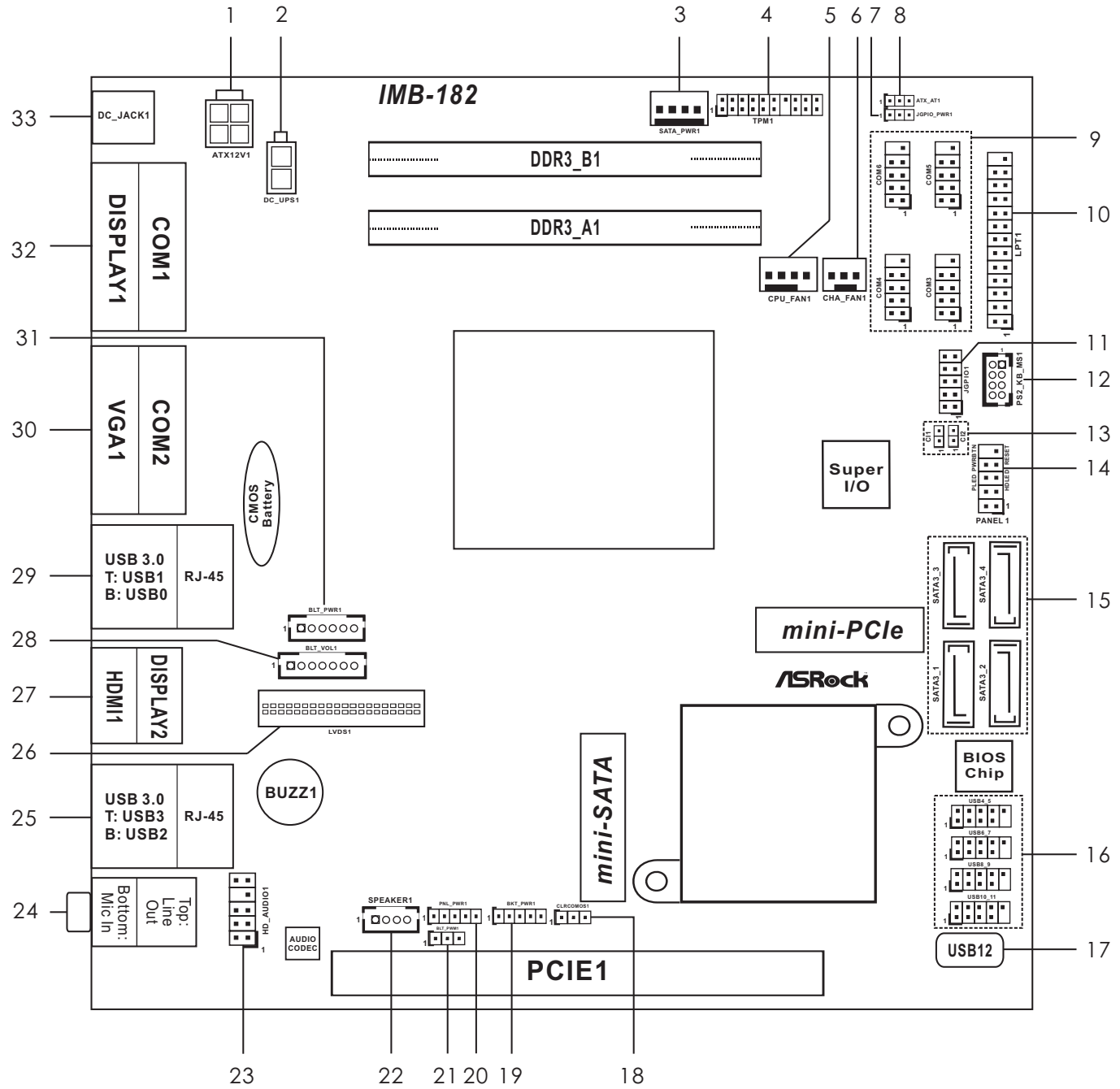




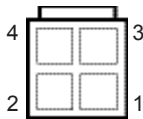
ASRock Jumpers and headers setting guide

IMB-182

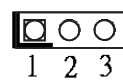
The terms HDMI™ and HDMI High-Definition Multimedia Interface, and the HDMI logo are trademarks or registered trademarks of HDMI Licensing LLC in the United States and other countries.



1 : ATX Power Connector (Input 9V-19V)
1-2: GND
3-4: DC Input

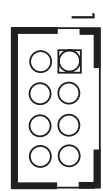


8 : ATX/AT Mode Jumper
1-2: AT Mode
2-3: ATX Mode



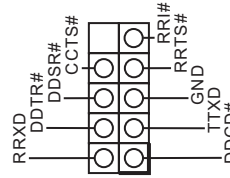
12 : PS2_KB_MS1

PIN	Signal Name
1	KBCLK
2	+5V
3	KBDATA
4	+5V
5	MSDATA
6	GND
7	MSCLK
8	GND

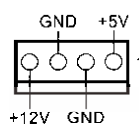


2 : 2-pin UPS Module Power Input Connector

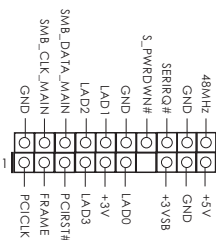
9 : COM3, 4, 5, 6 Headers (RS232)



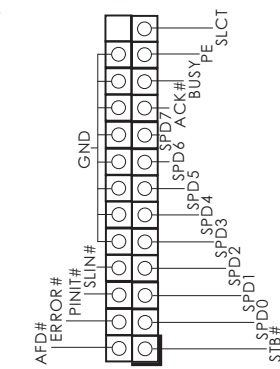
3 : SATA Power Output Connector



4 : TPM Header



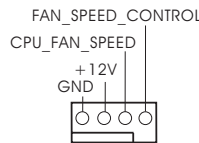
10 : Printer Port Header



13 : Chassis Intrusion Headers
CI1:
Close: Active case open
Open: Normal
CI2:
Close: Normal
Open: Active case open

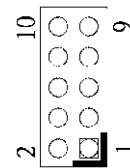


5 : 4-Pin CPU FAN Connector (+12V)

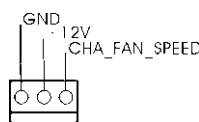


11 : Digital Input / Output Pin Header

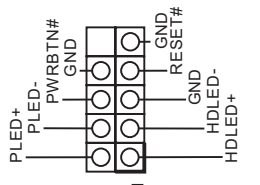
PIN	Signal Name	PIN	Signal Name
10	GND	9	JGPIOW_PWR1
8	SIO_GP23	7	SIO_GP27
6	SIO_GP22	5	SIO_GP26
4	SIO_GP21	3	SIO_GP25
2	SIO_GP20	1	SIO_GP24



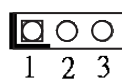
6 : 3-Pin Chassis FAN Connector (+12V)



14 : System Panel Header

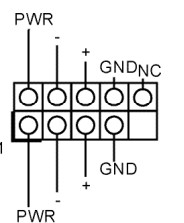


7 : Digital Input / Output Power select
1-2: +12V
2-3: +5V

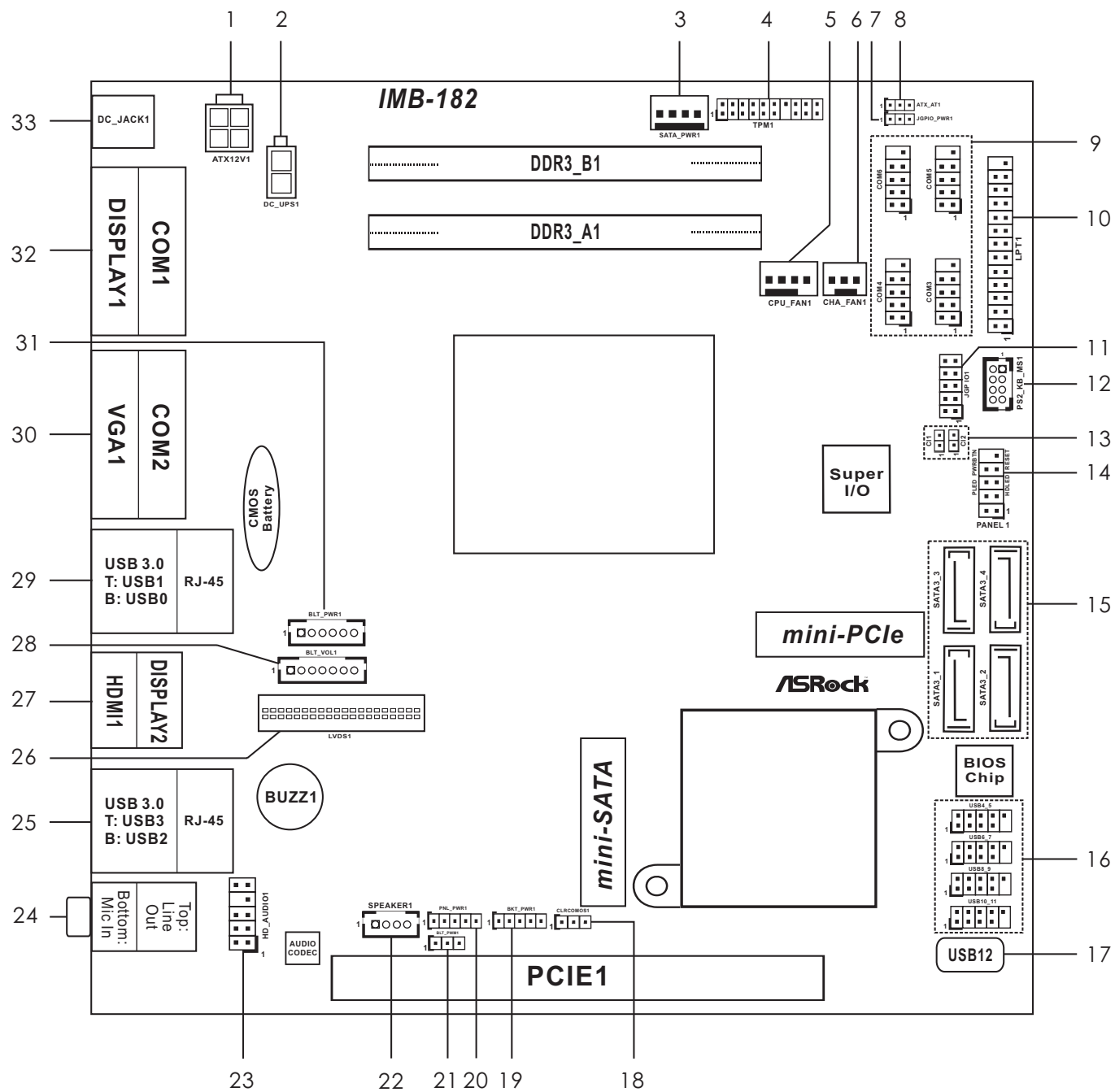


15 : SATA3 Connectors

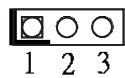
16 : USB2.0 Connectors



17 : USB2.0 Port (USB12)

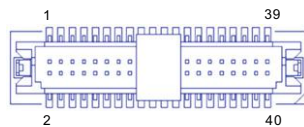


18 : Clear CMOS Header
 1-2: Normal
 2-3: Clear CMOS



25 : Top : RJ45 LAN Port2
 Bottom : USB3.0 (USB_2_3)

26 : LVDS Panel Connector



PIN	Signal Name	PIN	Signal Name
1	LVDD	2	LVDD
3	+3V	4	N/A
5	N/A	6	LVDS_A_DATA0#
7	LVDS_A_DATA0	8	GND1
9	LVDS_A_DATA1#	10	LVDS_A_DATA1
11	GND6	12	LVDS_A_DATA2#
13	LVDS_A_DATA2	14	GND2
15	LVDS_A_DATA3#	16	LVDS_A_DATA3
17	GND7	18	LVDS_A_CLK#
19	LVDS_A_CLK	20	GND3
21	LVDS_B_DATA0#	22	LVDS_B_DATA0
23	GND8	24	LVDS_B_DATA1#
25	LVDS_B_DATA1	26	GND4
27	LVDS_B_DATA2#	28	LVDS_B_DATA2
29	DPLVDD_EN	30	LVDS_B_DATA3#
31	LVDS_B_DATA3	32	GND5
33	LVDS_B_CLK#	34	LVDS_B_CLK
35	GND9	36	CON_LBKL_TEN_R
37	CON_LBKL_CTR_R	38	LCD_BLT_VCC
39	LCD_BLT_VCC	40	LCD_BLT_VCC

28 : Backlight Volume Control

PIN	Signal Name
1	GPIO_VOL_UP
2	GPIO_VOL_DW
3	PWRDN
4	LVDS1 BLUP
5	LVDS1 BLDW
6	GND
7	GND



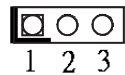
19 : BKT_PWR1
 1-2: LCD_BLT_VCC: +5V
 2-3: LCD_BLT_VCC: +12V
 4-5: LCD_BLT_VCC: DC_IN



20 : PNL_PWR1
 1-2: LVDD: +3V
 2-3: LVDD: +5V
 4-5: LVDD: +12V



21 : BLT_PWM1
 1-2: +3V Level
 2-3: +5V Level

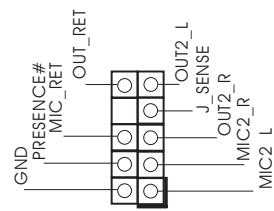


22 : 3W Audio AMP Output Wafer



PIN	Signal Name
1	SPK R-
2	SPK R+
3	SPK L+
4	SPK L-

23 : Front Panel Audio Header



24 : Audio Jacks
 Green - Line Out
 Pink - Mic In

27 : Top : DisplayPort
 Bottom : HDMI

29 : Top : RJ45 LAN Port1
 Bottom : USB3.0 (USB_0_1)

30 : Top : COM2*
 Bottom : VGA

31 : Backlight Power Connector

PIN	Signal Name
1	GND
2	GND
3	BL CTL
4	BL EN
5	LCD_BLT_VCC
6	LCD_BLT_VCC



32 : Top : COM1*
 Bottom : DisplayPort

* This motherboard supports RS232/422/485 on COM1, 2 ports. Please refer to below table for the pin definition. In addition, COM1, 2 ports (RS232/422/485) can be adjusted in BIOS setup utility > Advanced Screen > Super IO Configuration. You may refer to page 34 of our user manual for details.

COM1, 2 Port Pin Definition

PIN	RS232	RS422	RS485
1	DCD, Data Carrier Detect	TX-	RTX-
2	RXD, Receive Data	RX+	N/A
3	TXD, Transmit Data	TX+	RTX+
4	DTR, Data Terminal Ready	RX-	N/A
5	GND	GND	GND
6	DSR, Data Set Ready	N/A	N/A
7	RTS, Request To Send	N/A	N/A
8	CTS, Clear To Send	N/A	N/A
9	No Power/5V/12V	N/A	N/A

33 : DC Jack