



P/N: 15G06M036000AK V1.0

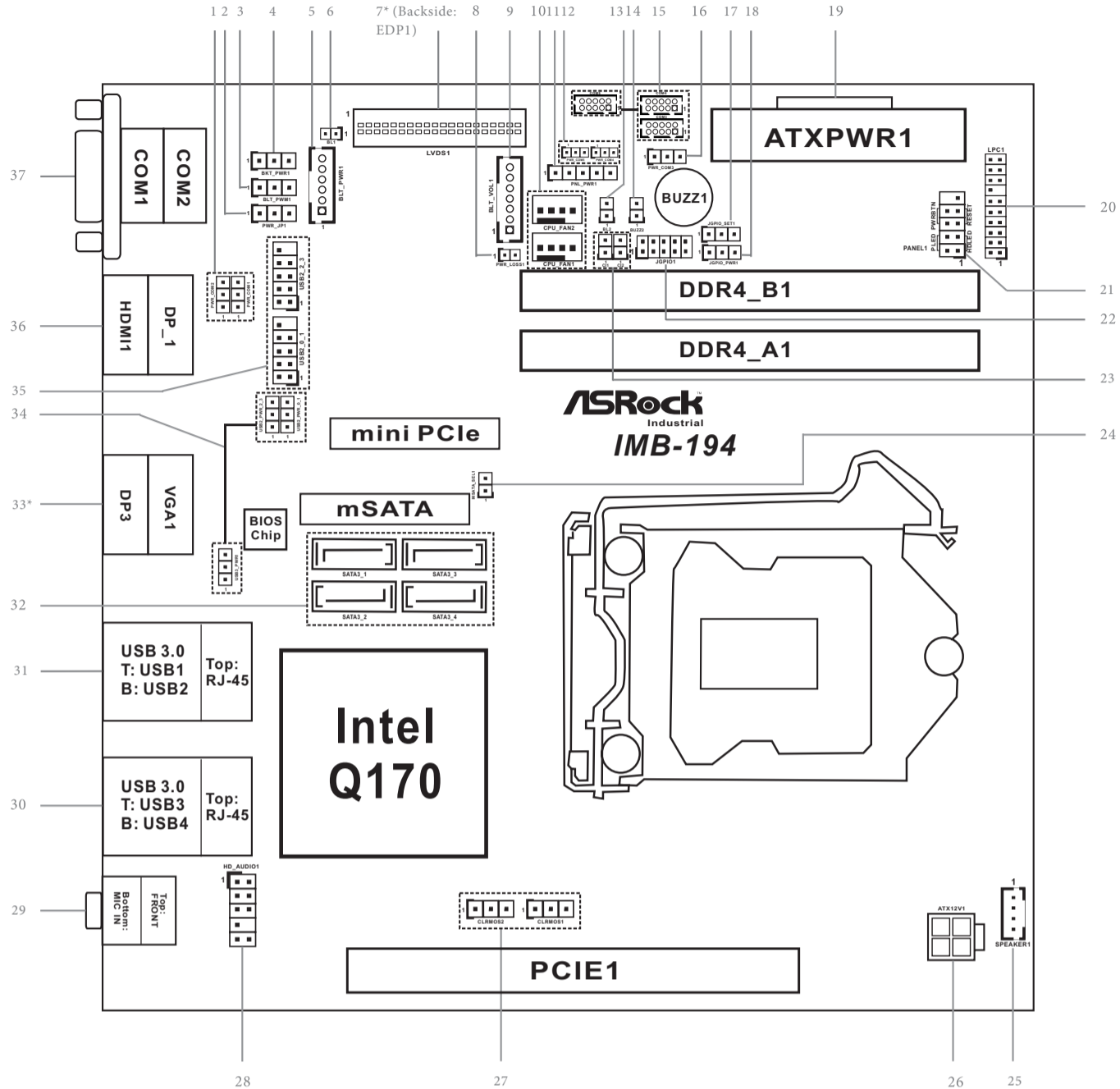


Jumpers and Headers Setting Guide

IMB-194-D

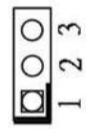
IMB-194-L

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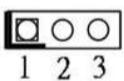
COM Port PWR Setting Jumpers

- 1 : PWR_COM1 (For COM Port1)
PWR_COM2 (For COM Port2)
- 12 : PWR_COM4 (For COM Port4)
PWR_COM5 (For COM Port5)
- 16 : PWR_COM3 (For COM Port3)
1-2 : +5V
2-3 : +12V



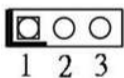
2 : ATX/AT Mode Select (PWR_JP1)

- 1-2 : AT Mode
- 2-3 : ATX Mode



3 : Backlight Control Level (BLT_PWM1)

- 1-2 : +3V (eDP)
- 2-3 : +5V (LVDS)



4 : Backlight Power Select (LCD_BLT_VCC) (BKT_PWR1)

- 1-2 : LCD_BLT_VCC : +5V
- 2-3 : LCD_BLT_VCC : +12V

SIGNAL	SIGNAL NAME	PIN	SIGNAL NAME	PIN	SIGNAL NAME	PIN	SIGNAL NAME	PIN	SIGNAL NAME	PIN
1	LCD_VCC	3	LDCC_DATA	5	LVDS_A_DATA0	7	LVDS_A_DATA1#	9	LVDS_A_DATA2#	11
2	LDDC_CLK	4	LVDS_A_DATA3#	6	LVDS_A_CLK#	8	LVDS_B_DATA0#	10	LVDS_B_DATA1#	12
3	LVDS_A_DATA0	5	LVDS_A_CLK#	8	LVDS_B_DATA2#	10	LVDS_B_DATA3#	12	LVDS_B_CLK#	14
4	LVDS_A_DATA1#	6	LVDS_B_DATA0#	10	LVDS_B_CLK#	14	CON_LBKLT_EN	31	CON_LBKLT_CTL	37
5	LVDS_A_DATA2#	7	LVDS_B_DATA1#	11	CON_LBKLT_VCC	32	LCD_BLT_VCC	37	LCD_BLT_VCC	39
6	LVDS_A_DATA3#	8	LVDS_B_DATA2#	12	LCD_BLT_VCC	37				
7	LVDS_A_CLK#	9	LVDS_B_DATA3#	13						
8	LVDS_B_DATA0#	10	LVDS_B_CLK#	14						
9	LVDS_B_DATA1#	11								
10	LVDS_B_DATA2#	12								
11	LVDS_B_CLK#	14								
12	CON_LBKLT_EN	31								
13	CON_LBKLT_CTL	37								
14	LCD_BLT_VCC	37								
15	LCD_BLT_VCC	39								

* eDP Connector (For IMB-194-D only) (on the Backside of PCB)



SIGNAL	SIGNAL NAME	PIN	SIGNAL NAME	PIN	SIGNAL NAME	PIN	SIGNAL NAME	PIN	SIGNAL NAME	PIN
1	NA		CON_LBKLT_CTL	31	CON_LBKLT_VCC	32	LCD_BLT_VCC	37	LCD_BLT_VCC	39
2	NA		CON_LBKLT_EN	31	LCD_BLT_VCC	37				
3	NA		LCD_BLT_VCC	37						
4	NA		LCD_BLT_VCC	39						
5	NA									
6	NA									
7	NA									
8	NA									
9	NA									
10	NA									
11	NA									
12	NA									
13	NA									
14	NA									
15	NA									
16	NA									
17	NA									
18	NA									
19	NA									
20	NA									
21	NA									
22	NA									
23	NA									
24	NA									
25	NA									
26	NA									
27	NA									
28	NA									
29	NA									
30	NA									
31	NA									
32	NA									
33	NA									
34	NA									
35	NA									
36	NA									
37	NA									
38	NA									
39	NA									
40	NA									

- 11 : Panel Power Select (LCD_VCC) (PNL_PWR1)
1-2 : LCD_VCC : +3V
2-3 : LCD_VCC : +5V
4-5 : LCD_VCC : +12V



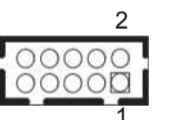
13 : BL2



14 : Buzzer



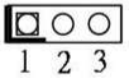
15 : COM3, 4, 5 Headers (RS232)



PIN	SIGNAL NAME	PIN	SIGNAL NAME	PIN	SIGNAL NAME	PIN	SIGNAL NAME	PIN	SIGNAL NAME
10	NC	8	CCTS#	6	DDSR#	4	DDTR#	2	RRXD
9	+5V / +12V	7	RRTS#	5	GND	3	TTXD	1	DDCD#

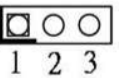
17 : Digital Input / Output Default Value Setting (JGPIO_SET1)

- 1-2 : Pull-High
- 2-3 : Pull-Low

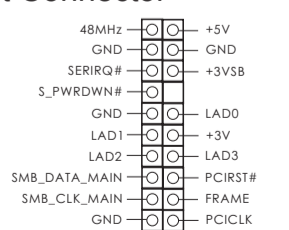
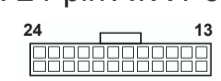


18 : Digital Input / Output Power Select (JGPIO_PWR) (JGPIO_PWR1)

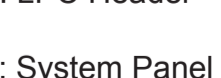
- 1-2 : +12V
- 2-3 : +5V



19 : 24-pin ATX Power Input Connector



20 : LPC Header



21 : System Panel Header



5 : Inverter Power Control Wafer (BLT_PWR1)

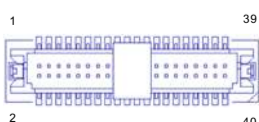


PIN	SIGNAL NAME
6	LCD_BLT_VCC
5	LCD_BLT_VCC
4	CON_LBKLT_EN
3	CON_LBKLT_CTL
2	GND
1	GND

6 : BL1

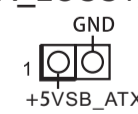


7* : LVDS Panel Connector (For IMB-194-L only)



8 : Power Loss Jumper (PWR_LOSS1)

- Open: Power Loss
- Close: no Power Loss

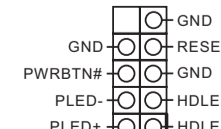
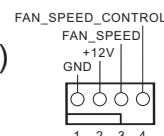


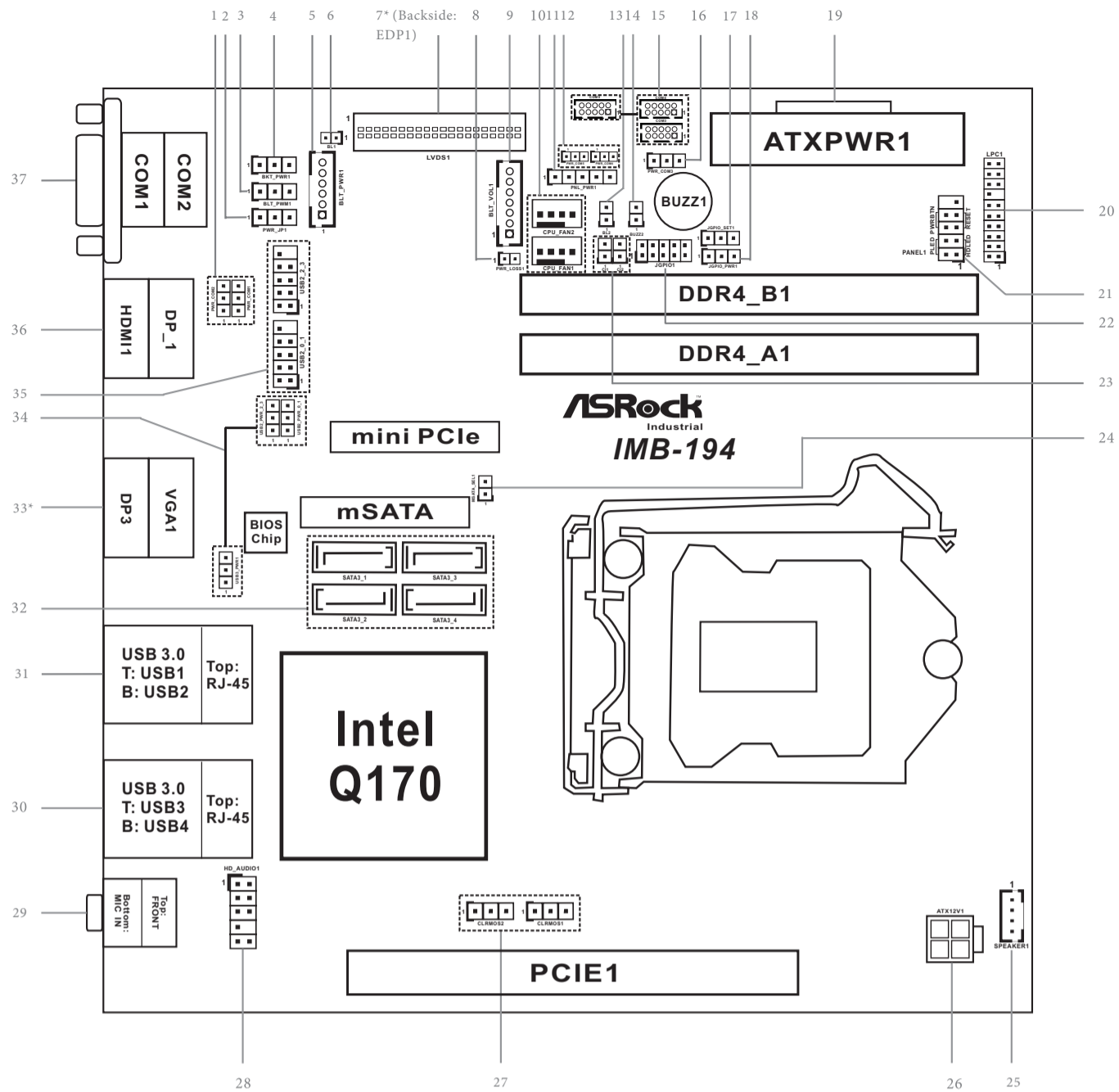
9 : Backlight Volume Control (BLT_VOL1)



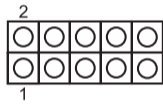
PIN	SIGNAL NAME
7	GND
6	GND
5	BLT_DW
4	BLT_UP
3	PWRDN
2	GPIO_VOL_DW
1	GPIO_VOL_UP

10 : 4-Pin CPU FAN Connectors (+12V) (CPU_FAN1, CPU_FAN2)





22 : Digital Input/Output Pin Header (JGPIO1)

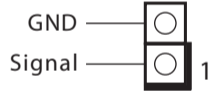


PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
2	SIO_GP20	4	SIO_GP21	6	SIO_GP22	8	SIO_GP23	10	GND
1	SIO_GP24	3	SIO_GP25	5	SIO_GP26	7	SIO_GP27	9	JGPIO_PWR

23 : Chassis Intrusion Headers

C11 :
 Close : Active Case Open
 Open : Normal

C12 :
 Close : Normal
 Open : Active Case Open

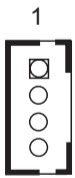


24 : MSATA Select (MSATA_SEL1)

Open : For SATA3_1
 Short : For mSATA

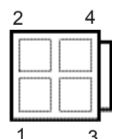


25 : 3W Audio AMP Output Wafer



PIN	Signal Name
1	OUTLN
2	OUTLP
3	OUTRP
4	OUTRN

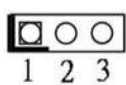
26 : 4-pin ATX Power Input/Output Connector



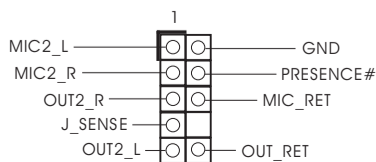
27 : Clear CMOS Headers

CLRMO1 :
 1-2 : Normal
 2-3 : Clear CMOS

CLRMO2 :
 1-2 : +BATT
 2-3 : +3VSB_A



28 : Front Panel Audio Header



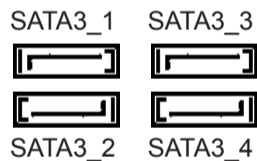
29 : Audio Output

Green - Line Out
 Pink - Mic In

**30 : Top : RJ45 LAN Port (LAN2)
 Bottom : USB3.0 Ports (USB3_34)**

**31 : Top : RJ45 LAN Port (LAN1)
 Bottom : USB3.0 Ports (USB3_12)**

32 : SATA3 Connectors (SATA3_1, SATA3_2, SATA3_3, SATA3_4)

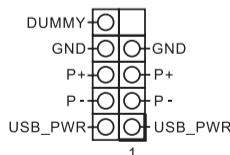


**33* : Top : D-Sub Port (VGA1) (For IMB-194-L only)
 Bottom : DisplayPort (DP3) (For IMB-194-D only)**

**34 : USB Power Setting Jumpers (USB2_PWR_0_1 (For USB2_0_1))
 (USB2_PWR_2_3 (For USB2_2_3))
 (USB3_PWR1 (For USB3_12, USB3_34))**
 1-2 : +5V
 2-3 : +5VSB



35 : USB2.0 Headers (USB2_0_1, USB2_2_3)



**36 : Top : DisplayPort (DP_1)
 Bottom : HDMI Port (HDMI1)**

**37 : Top : COM Port (COM2) (RS232/422/485)
 Bottom : COM Port (COM1) (RS232/422/485)**

* This motherboard supports RS232/422/485 on COM1, 2 ports. Please refer to below table for the pin definition. In addition, COM1, 2 ports (RS232/422/485) can be adjusted in BIOS setup utility > Advanced Screen > Super IO Configuration. You may refer to our user manual for details.

COM1, 2 Port Pin Definition

PIN	RS232	RS422	RS485
1	DCD	TX-	RTX-
2	RXD	RX+	N/A
3	TXD	TX+	RTX+
4	DTR	RX-	N/A
5	GND	GND	GND
6	DSR	N/A	N/A
7	RTS	N/A	N/A
8	CTS	N/A	N/A
9	+5V / +12V	N/A	N/A