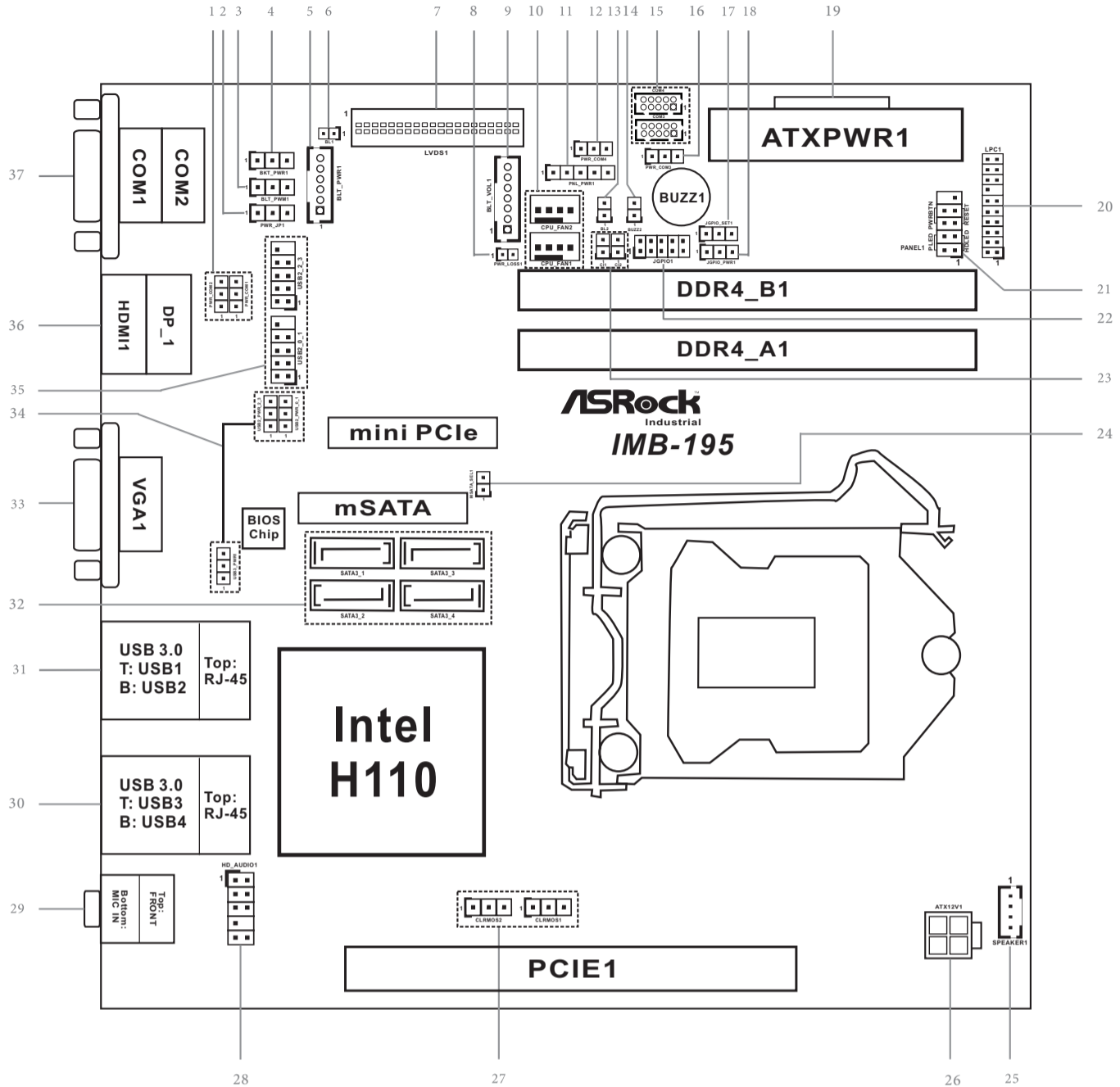




# ASRock Jumpers and Headers Setting Guide

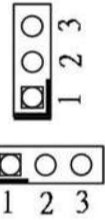
## IMB-195

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### COM Port PWR Setting Jumpers

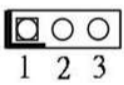
- 1 : PWR\_COM1 (For COM Port1)
- PWR\_COM2 (For COM Port2)
- 12 : PWR\_COM4 (For COM Port4)
- 16 : PWR\_COM3 (For COM Port3)



- 1-2 : +5V
- 2-3 : +12V

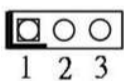
### 2 : ATX/AT Mode Select (PWR\_JP1)

- 1-2 : AT Mode
- 2-3 : ATX Mode



### 3 : Backlight Control Level (BLT\_PWM1)

- 1-2 : +3V
- 2-3 : +5V



### 4 : Backlight Power Select (LCD\_BLT\_VCC) (BKT\_PWR1)

- 1-2 : LCD\_BLT\_VCC : +5V
- 2-3 : LCD\_BLT\_VCC : +12V

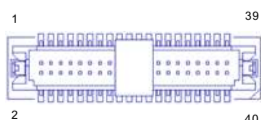
### 5 : Inverter Power Control Wafer (BLT\_PWR1)

### 6 : BL1



PIN	Signal Name
6	LCD_BLT_VCC
5	LCD_BLT_VCC
4	CON_LBKLT_EN
3	CON_LBKLT_CTL
2	GND
1	GND

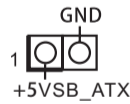
### 7 : LVDS Panel Connector



Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN
LCD_VCC	1	LCD_VCC	3	LDDC_DATA	5	LVDS_A_DATA0	7	LVDS_A_DATA1#	9
LDDC_CLK	4	LDDC_DATA#	6	LVDS_A_DATA2#	10	LVDS_A_DATA3#	12	LVDS_A_DATA4#	14
LVDS_A_DATA0#	6	GND	8	LVDS_A_DATA1#	10	LVDS_A_DATA2#	12	LVDS_A_DATA3#	14
GND	8	LVDS_A_DATA2#	10	LVDS_A_DATA3#	12	LVDS_A_DATA4#	14	LVDS_A_CLK#	16
LVDS_A_DATA1#	10	LVDS_A_DATA2#	12	LVDS_A_DATA3#	14	LVDS_A_DATA4#	16	LVDS_A_CLK	18
LVDS_A_DATA2#	12	LVDS_A_DATA3#	14	LVDS_A_DATA4#	16	LVDS_B_DATA0#	20	LVDS_B_DATA1	22
LVDS_A_DATA3#	14	LVDS_A_DATA4#	16	LVDS_B_DATA0#	20	LVDS_B_DATA1	22	LVDS_B_DATA2#	24
LVDS_A_DATA4#	16	LVDS_B_DATA0#	20	LVDS_B_DATA1	22	LVDS_B_DATA2#	24	DPLVDD_EN	26
LVDS_B_DATA0#	20	LVDS_B_DATA1	22	LVDS_B_DATA2#	24	DPLVDD_EN	26	LVDS_B_DATA3	28
LVDS_B_DATA1	22	LVDS_B_DATA2#	24	DPLVDD_EN	26	LVDS_B_DATA3	28	LVDS_B_CLK#	30
LVDS_B_DATA2#	24	DPLVDD_EN	26	LVDS_B_CLK#	30	CON_LBKLT_EN	32	CON_LBKLT_CTL	34
DPLVDD_EN	26	LVDS_B_CLK#	30	CON_LBKLT_EN	32	CON_LBKLT_CTL	34	LCD_BLT_VCC	36
LVDS_B_DATA3	28	CON_LBKLT_EN	32	CON_LBKLT_CTL	34	LCD_BLT_VCC	36	LCD_BLT_VCC	38
LVDS_B_CLK#	30	CON_LBKLT_CTL	34	LCD_BLT_VCC	36	LCD_BLT_VCC	38		40
CON_LBKLT_EN	32	LCD_BLT_VCC	36		40				
CON_LBKLT_CTL	34								
LCD_BLT_VCC	36								
LCD_BLT_VCC	38								
	40								

### 8 : Power Loss Jumper (PWR\_LOSS1)

- Open: Power Loss
- Close: no Power Loss

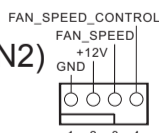


### 9 : Backlight Volume Control (BLT\_VOL1)



PIN	Signal Name
7	GND
6	GND
5	BLT_DW
4	BLT_UP
3	PWRDN
2	GPIO_VOL_DW
1	GPIO_VOL_UP

### 10 : 4-Pin CPU FAN Connectors (+12V) (CPU\_FAN1, CPU\_FAN2)



### 11 : Panel Power Select (LCD\_VCC) (PNL\_PWR1)

- 1-2 : LCD\_VCC : +3V
- 2-3 : LCD\_VCC : +5V
- 4-5 : LCD\_VCC : +12V



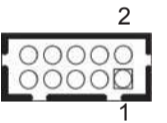
### 13 : BL2



### 14 : Buzzer



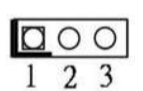
### 15 : COM3, 4 Headers (RS232)



PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
10	NC	8	CCTS#	6	DDSR#	4	DDTR#	2	RRXD
9	+5V / +12V	7	RRTS#	5	GND	3	TTXD	1	DDCD#

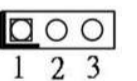
### 17 : Digital Input / Output Default Value Setting (JGPIO\_SET1)

- 1-2 : Pull-High
- 2-3 : Pull-Low

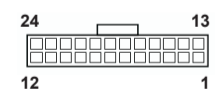


### 18 : Digital Input / Output Power Select (JGPIOPWR) (JGPIO\_PWR1)

- 1-2 : +12V
- 2-3 : +5V

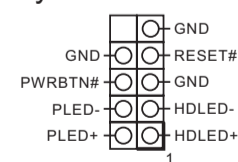


### 19 : 24-pin ATX Power Input Connector

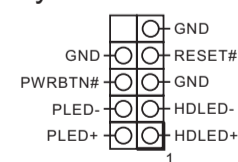


48MHz	+5V
GND	GND
SERIRQ#	+3VSB
S_PWRDWN#	LAD0
GND	+3V
LAD1	LAD3
LAD2	PCIRST#
SMB_DATA_MAIN	FRAME
SMB_CLK_MAIN	PCICLK
GND	

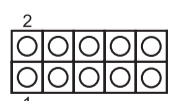
### 20 : LPC Header



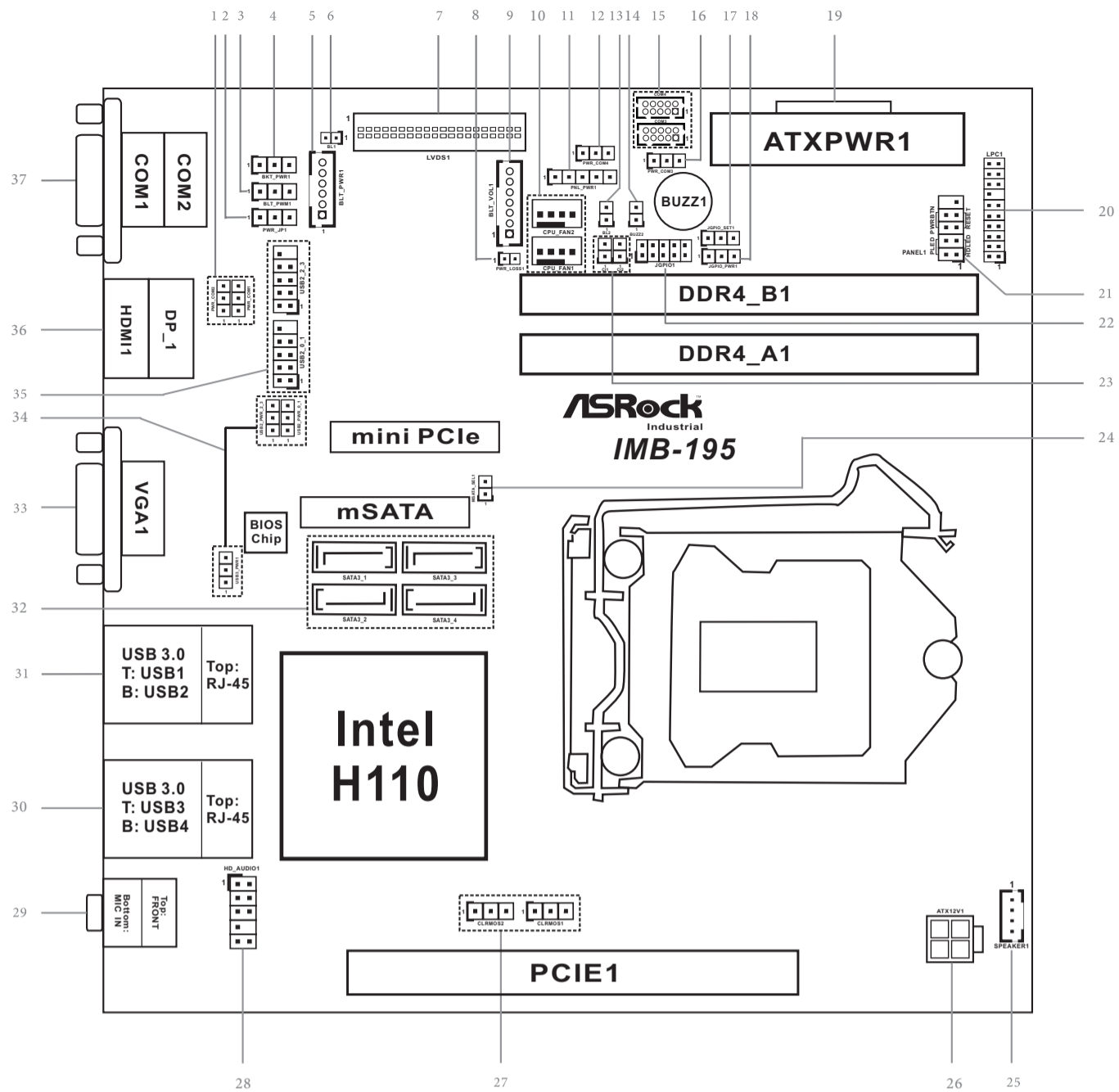
### 21 : System Panel Header



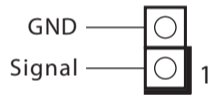
### 22 : Digital Input/Output Pin Header (JGPIO1)



PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
2	SIO_GP20	4	SIO_GP21	6	SIO_GP22	8	SIO_GP23	10	GND
1	SIO_GP24	3	SIO_GP25	5	SIO_GP26	7	SIO_GP27	9	JGPIO_PWR



23 : Chassis Intrusion Headers  
 CI1 :  
 Close : Active Case Open  
 Open : Normal  
 CI2 :  
 Close : Normal  
 Open : Active Case Open



31 : Top : RJ45 LAN Port (LAN1)  
 Bottom : USB3.0 Ports (USB3\_12)

32 : SATA3 Connectors  
 (SATA3\_1, SATA3\_2, SATA3\_3,  
 SATA3\_4)

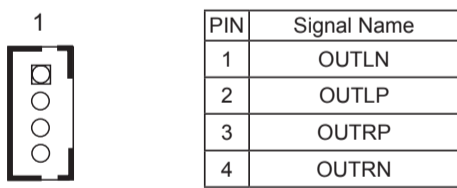


24 : MSATA Select (MSATA\_SEL1)  
 Open : For SATA3\_1  
 Short : For mSATA

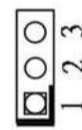


33 : D-Sub Port (VGA1)

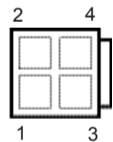
25 : 3W Audio AMP Output Wafer



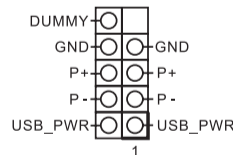
34 : USB Power Setting Jumpers  
 (USB2\_PWR\_0\_1 (For USB2\_0\_1))  
 (USB2\_PWR\_2\_3 (For USB2\_2\_3))  
 (USB3\_PWR1 (For USB3\_12, USB3\_34))  
 1-2 : +5V  
 2-3 : +5VSB



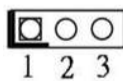
26 : 4-pin ATX Power Input/Output Connector



35 : USB2.0 Headers (USB2\_0\_1, USB2\_2\_3)

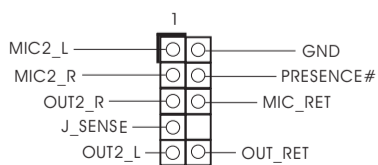


27 : Clear CMOS Headers  
 CLRMOS1 :  
 1-2 : Normal  
 2-3 : Clear CMOS  
 CLRMOS2 :  
 1-2 : +BATT  
 2-3 : +3VSB\_A



36 : Top : DisplayPort (DP\_1)  
 Bottom : HDMI Port (HDM11)  
 37 : Top : COM Port (COM2) (RS232)  
 Bottom : COM Port (COM1)  
 (RS232/422/485)

28 : Front Panel Audio Header



29 : Audio Output  
 Green - Line Out  
 Pink - Mic In

30 : Top : RJ45 LAN Port (LAN2)  
 Bottom : USB3.0 Ports (USB3\_34)

\* This motherboard supports RS232/422/485 on COM1 port. Please refer to below table for the pin definition. In addition, COM1 port (RS232/422/485) can be adjusted in BIOS setup utility > Advanced Screen > Super IO Configuration. You may refer to our user manual for details.

**COM1 Port Pin Definition**

PIN	RS232	RS422	RS485
1	DCD	TX-	RTX-
2	RXD	RX+	N/A
3	TXD	TX+	RTX+
4	DTR	RX-	N/A
5	GND	GND	GND
6	DSR	N/A	N/A
7	RTS	N/A	N/A
8	CTS	N/A	N/A
9	+5V / +12V	N/A	N/A