

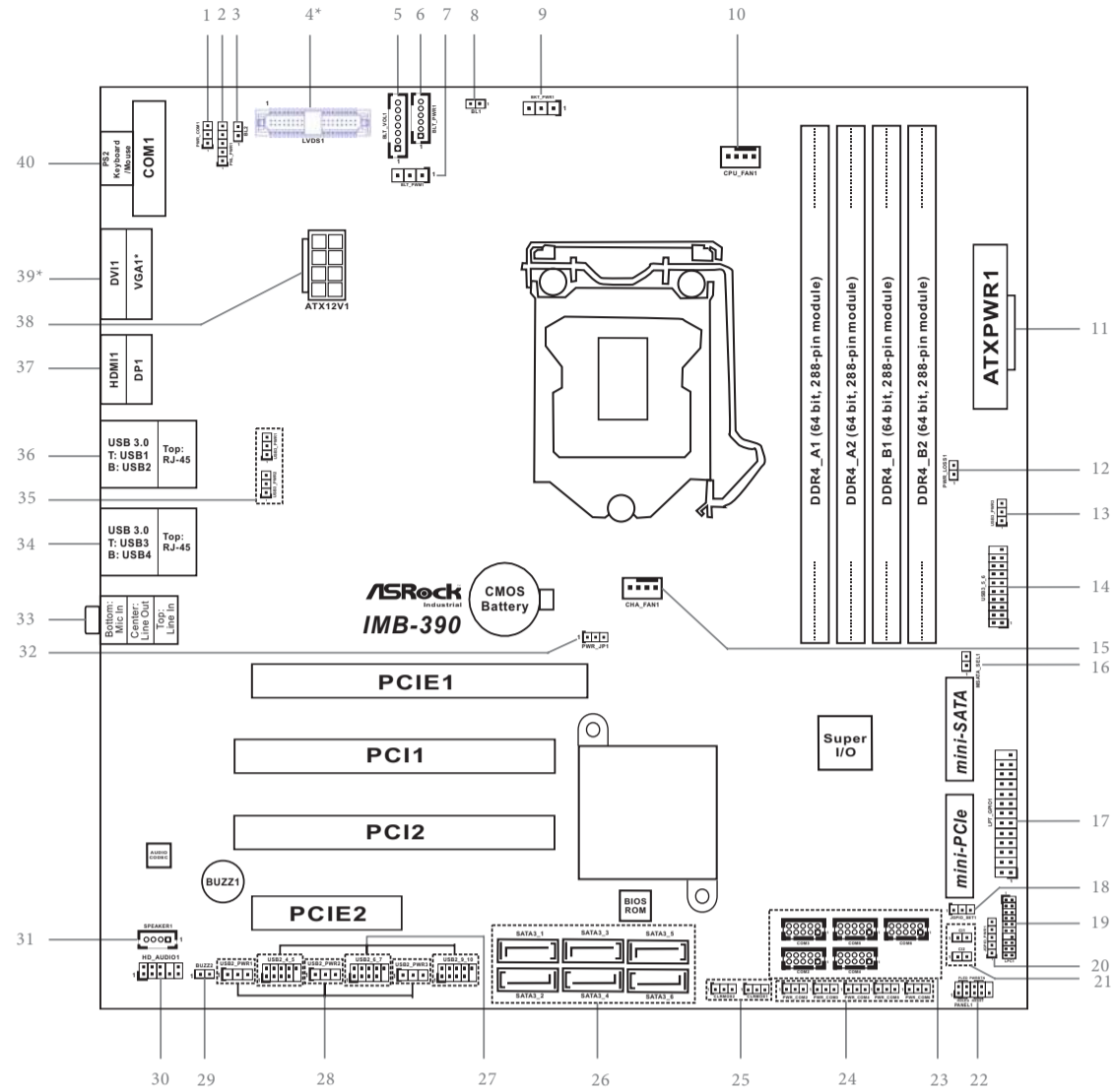


P/N: 15G06M046000AK V1.0

ASRock® Jumpers and Headers Setting Guide

IMB-390-L IMB-390-D

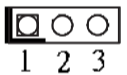
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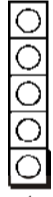
COM Port Pin9 PWR Setting Jumpers
1 : PWR_COM1 (For COM Port1)



24 : PWR_COM2 (For COM Port2)
PWR_COM3 (For COM Port3)
PWR_COM4 (For COM Port4)
PWR_COM5 (For COM Port5)
PWR_COM6 (For COM Port6)



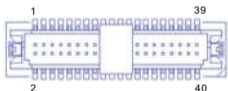
2 : Panel Power Select (PNL_PWR1)
1-2 : +3V
2-3 : +5V
4-5 : +12V



3 : BL2
Open: Protect LCD_VCC
Short: No Protect LCD_VCC



4* : LVDS Panel Connector
(Only for IMB-390-L)



PIN	Signal Name	PIN	Signal Name
1	R_LVDD	17	GND
2	R_LVDD	18	LVDS_A_DATA#
3	LDDC_CLK	19	LVDS_B_DATA#
4	LVDS_A_DATA#	20	LVDS_C_DATA#
5	LDDC_DATA	21	LVDS_D_DATA#
6	LVDS_B_DATA#	22	LVDS_E_DATA#
7	LVDS_A_DATA#	23	LVDS_F_DATA#
8	GND	24	LVDS_G_DATA#
9	LVDS_A_DATA#	25	LVDS_H_DATA#
10	LVDS_B_DATA#	26	LVDS_I_DATA#
11	GND	27	LVDS_J_DATA#
12	LVDS_A_DATA#	28	LVDS_K_DATA#
13	LVDS_B_DATA#	29	LVDS_L_DATA#
14	GND	30	LVDS_M_DATA#
15	LVDS_A_DATA#	31	LVDS_N_DATA#
16	LVDS_B_DATA#	32	LVDS_O_DATA#
17	GND	33	LVDS_P_DATA#
18	LVDS_A_DATA#	34	LVDS_Q_DATA#
19	LVDS_B_DATA#	35	LVDS_R_DATA#
20	LVDS_C_DATA#	36	LVDS_S_DATA#
21	LVDS_D_DATA#	37	LVDS_T_DATA#
22	LVDS_E_DATA#	38	LVDS_U_DATA#
23	LVDS_F_DATA#	39	LVDS_V_DATA#
24	LVDS_G_DATA#	40	LVDS_W_DATA#
25	LVDS_H_DATA#		
26	LVDS_I_DATA#		
27	LVDS_J_DATA#		
28	LVDS_K_DATA#		
29	LVDS_L_DATA#		
30	LVDS_M_DATA#		
31	LVDS_N_DATA#		
32	LVDS_O_DATA#		
33	LVDS_P_DATA#		
34	LVDS_Q_DATA#		
35	LVDS_R_DATA#		
36	LVDS_S_DATA#		
37	LVDS_T_DATA#		
38	LVDS_U_DATA#		
39	LVDS_V_DATA#		
40	LVDS_W_DATA#		

5 : Backlight & Amp Volume Control
(BLT_VOL1)



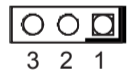
PIN	Signal Name
7	GND
6	GND
5	GPIO_BLT_DW
4	GPIO_BLT_UP
3	PWRDN
2	GPIO_VOL_DW
1	GPIO_VOL_UP

6 : Inverter Power Control Wafer
(BLT_PWR1)



PIN	Signal Name
1	GND
2	GND
3	CON_LBKLT_CTL
4	CON_LBKLT_EN
5	LCD_BLT_VCC
6	LCD_BLT_VCC

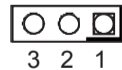
7 : Backlight Control Level (BLT_PWM1)
(CON_LBKLT_CTL)
1-2 : +3V (eDP)
2-3 : +5V (LVDS)



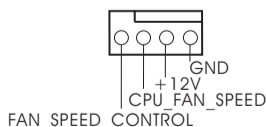
8 : BL1
Open: Protect LCD_BLT_VCC
Short: No Protect LCD_BLT_VCC



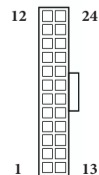
9 : Backlight Power Select
(LCD_BLT_VCC) (BKT_PWR1)
1-2 : LCD_BLT_VCC : +5V
2-3 : LCD_BLT_VCC : +12V



10 : CPU FAN Connector (+12V)



11 : 24-pin ATX Power Input Connector



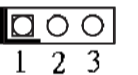
12 : PWR LOSS Header (PWR_LOSS1)
Short : Power Loss
Close : no Power Loss



USB Power Setting Jumpers
13 : USB3_PWR3 (For USB3_5_6)



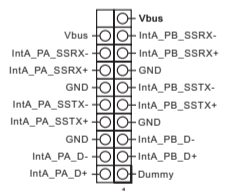
28 : USB2_PWR1 (For USB2_4_5)
USB2_PWR2 (For USB2_6_7)
USB2_PWR3 (For USB2_9_10)



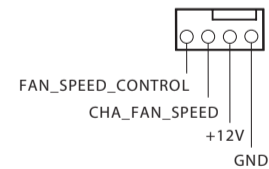
35 : USB3_PWR1 (For USB3_1_2)
USB3_PWR2 (For USB3_3_4)



14 : USB 3.0 Header
(USB3_5_6)



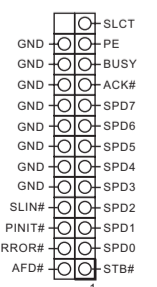
15 : Chassis FAN Connector (+12V)



16 : MSATA Select (MSATA_SEL1)
(mini-SATA is shared with SATA3_6)
Short : Use mini-SATA
Open : Use SATA3_6

17 : Printer Port / GPIO Header (LPT_GPIO1)

Printer Port:

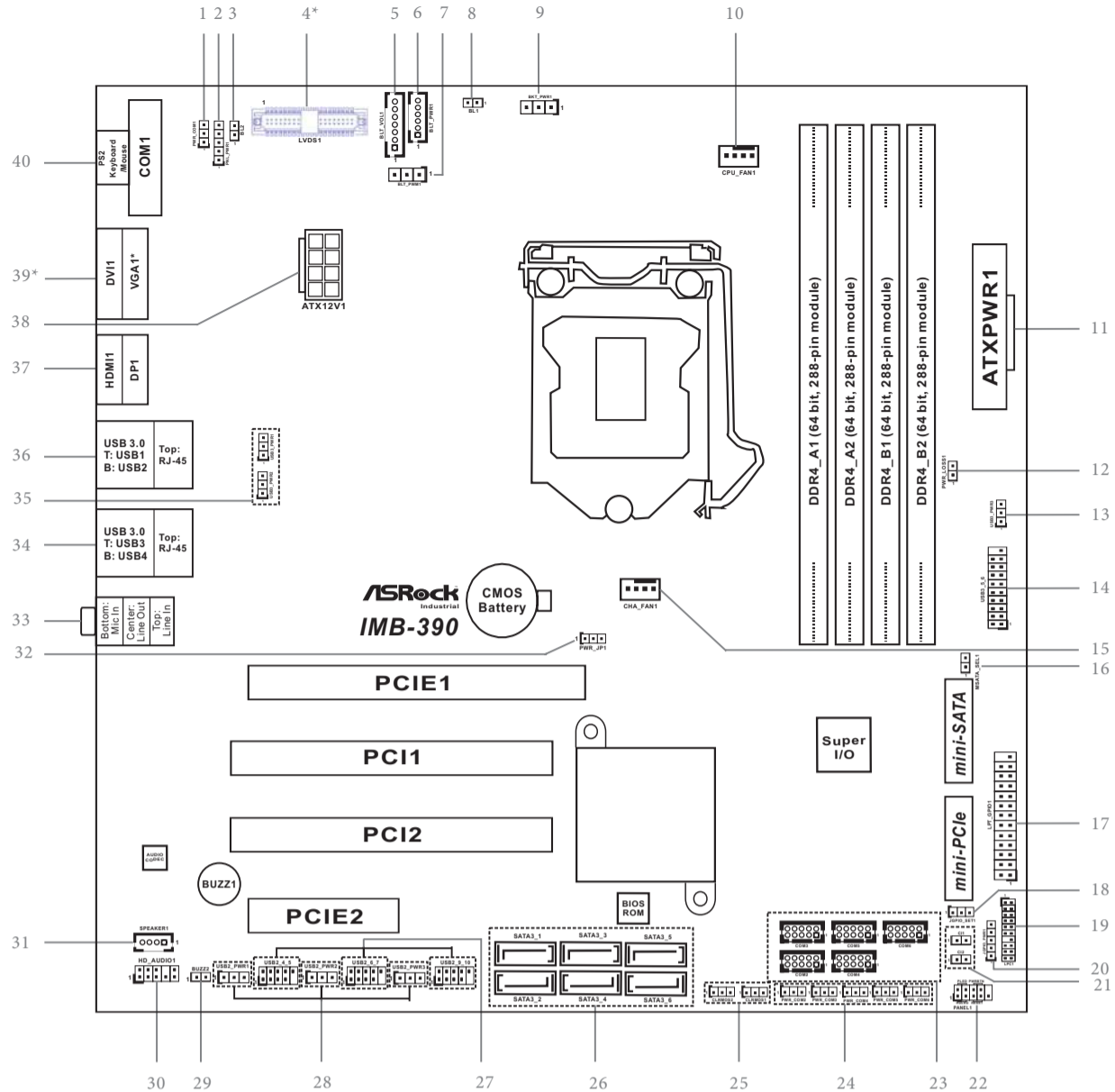


GPIO:

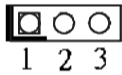
PIN	Signal Name	PIN	Signal Name
26	NC	25	NA
24	GND	23	SIO_GP30
22	GND	21	SIO_GP31
20	GND	19	SIO_GP32
18	GND	17	SIO_GP33
16	GND	15	SIO_GP34
14	GND	13	SIO_GP35
12	JGPIO_PWR	11	SIO_GP36
10	JGPIO_PWR	9	SIO_GP37
8	SIO_GP43	7	SIO_GP40
6	SIO_GP44	5	SIO_GP41
4	SIO_GP45	3	SIO_GP42
2	SIO_GP46	1	SIO_GP47

* If you want to use the printer port function, please short pin4 and pin5 on Digital Input / Output Power Select (JGPIO_PWR1).

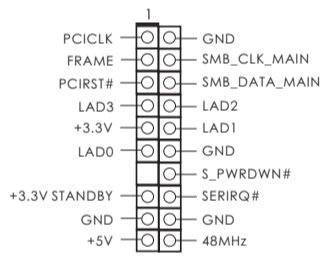
PIN	Signal Name
40	NA
39	LCD_BLT_VCC
38	LCD_BLT_VCC
37	LCD_BLT_VCC
36	LCD_BLT_VCC
35	SMB_CLK
34	SMB_DATA
33	CON_LBKLT_CTL
32	CON_LBKLT_EN
31	GND
30	GND
29	GND
28	GND
27	eDP_HPD_CON
26	GND
25	GND
24	GND
23	GND
22	NA
21	LCD_VCC
20	LCD_VCC
19	LCD_VCC
18	LCD_VCC
17	GND
16	eDP_AUX#_CON
15	eDP_AUX_CON
14	GND
13	eDP_TX0_CON
12	eDP_TX#0_CON
11	GND
10	eDP_TX1_CON
9	eDP_TX#1_CON
8	GND
7	eDP_TX2_CON
6	eDP_TX#2_CON
5	GND
4	eDP_TX3_CON
3	eDP_TX#3_CON
2	GND
1	NA



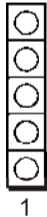
18 : Digital Input / Output Power Select (JGPIO_SET1)
 1-2 : +5V
 2-3 : GND



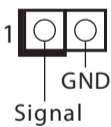
19 : LPC Header



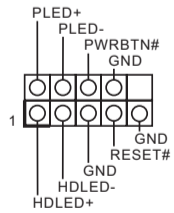
20 : Digital Input / Output Power Select (JGPIO_PWR) (JGPIO_PWR1)
 1-2 : +12V
 2-3 : +5V
 3-4 : +5V
 4-5 : GND



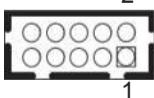
21 : Chassis Intrusion Headers (CI1, CI2)
 Short : Active Case Open
 Open : Normal



22 : System Panel Header

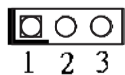


23 : COM Port Headers (COM2, 3, 4, 5, 6) (RS232)



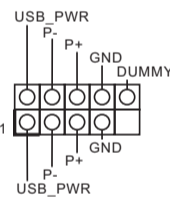
PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
10	N/A	8	CCTS#	6	DDSR#	4	DDTR#	2	RRXD
9	+5V	7	RRTS#	5	GND	3	TTXD	1	DDCD#

25 : Clear CMOS Headers
 CLRMOS1 :
 1-2 : Normal
 2-3 : Clear CMOS
 CLRMOS2 :
 1-2 : +BATT
 2-3 : +3VSB_A



26 : SATA3 Connectors (SATA3_1 ~ SATA3_6)
 (SATA3_6 is share with mini-SATA)

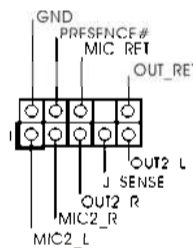
27 : USB 2.0 Headers (USB2_4_5, USB2_6_7, USB2_9_10)



29 : Buzzer (BUZZ2)



30 : Front Panel Audio Header



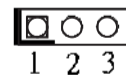
31 : 3W Audio AMP Output Wafer



PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
4	OUTRN	3	OUTRP	2	OUTLP	1	OUTLN

32 : ATX/AT Mode Jumper (PWR_JP1)

1-2 : AT Mode
 2-3 : ATX Mode



33 : Audio Jacks

Blue - Line In
 Green - Line Out
 Pink - Mic In

34 : Top : RJ45 LAN Port (LAN2)

Bottom : USB 3.0 Ports (USB3_3_4)

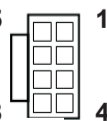
36 : Top : RJ45 LAN Port (LAN1)

Bottom : USB 3.0 Ports (USB3_1_2)

37 : Top : DisplayPort (DP1)

Bottom : HDMI Port (HDMI1)

38 : ATX 12V Power Connector



39 : Top : D-Sub Port (VGA1)*

(Only for IMB-390-L)
 Bottom : DVI Port (DVI1)

40 : Top : COM Port (COM1)
 (RS232/422/485)*

Bottom : PS/2 Keyboard/Mouse Port

* This motherboard supports RS232/422/485 on COM1 port. Please refer to below table for the pin definition. In addition, COM1 port (RS232/422/485) can be adjusted in BIOS setup utility > Advanced Screen > Super IO Configuration. You may refer to our user manual for details.

COM1 Port Pin Definition

PIN	RS232	RS422	RS485
1	DCD, Data Carrier Detect	TX-	RTX-
2	RXD, Receive Data	RX+	N/A
3	TXD, Transmit Data	TX+	RTX+
4	DTR, Data Terminal Ready	RX-	N/A
5	GND	GND	GND
6	DSR, Data Set Ready	N/A	N/A
7	RTS, Request To Send	N/A	N/A
8	CTS, Clear To Send	N/A	N/A
9	No Power/5V/12V	N/A	N/A