

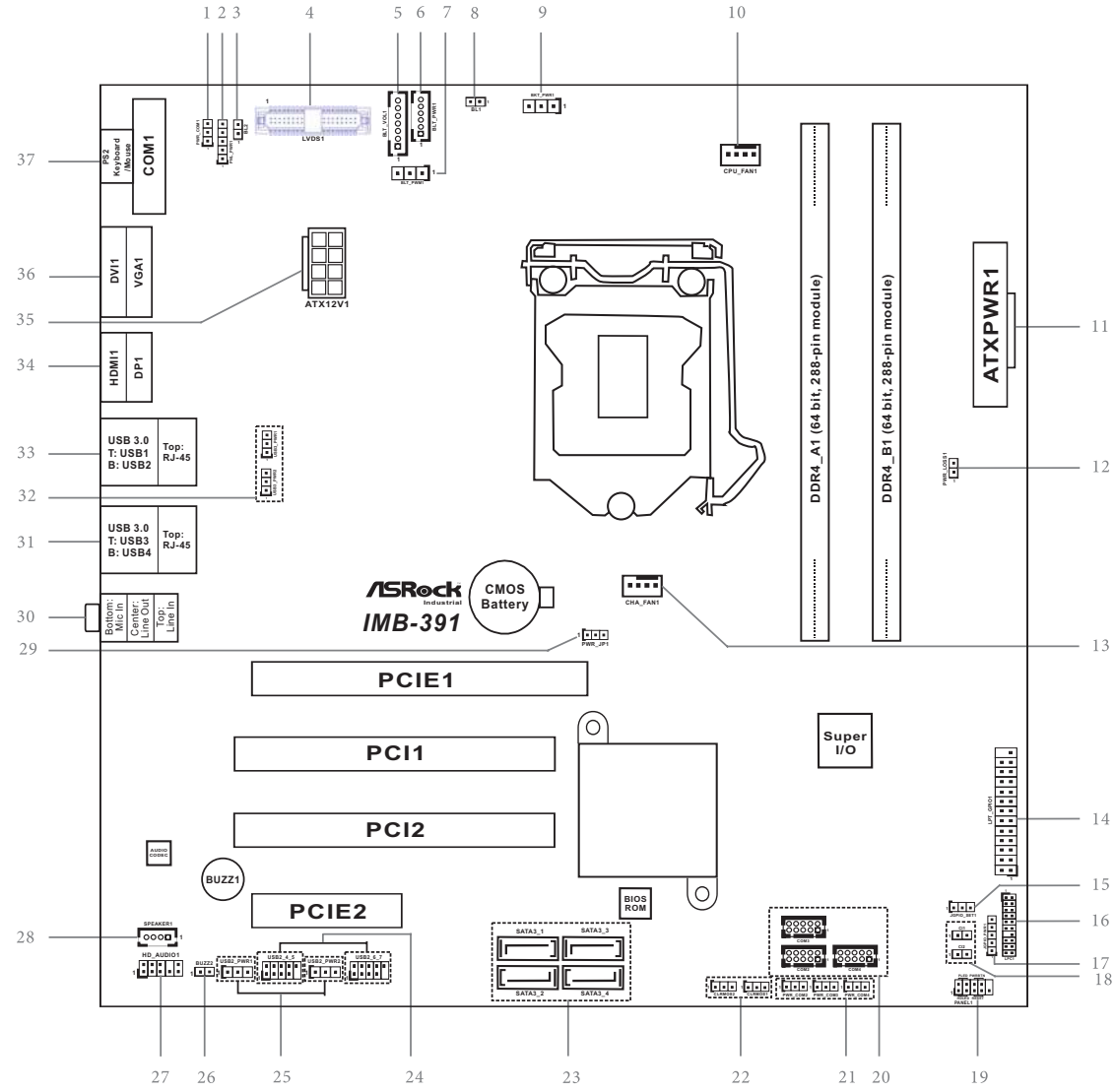


P/N: 15G06M047000AK V1.0

ASRock Jumpers and Headers Setting Guide

IMB-391

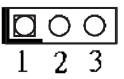
The terms HDMI™ and HDMI High-Definition Multimedia Interface, and the HDMI logo are trademarks or registered trademarks of HDMI Licensing LLC in the United States and other countries.



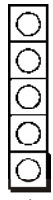
COM Port Pin9 PWR Setting Jumpers
1 : PWR_COM1 (For COM Port1)



21 : PWR_COM2 (For COM Port2)
PWR_COM3 (For COM Port3)
PWR_COM4 (For COM Port4)



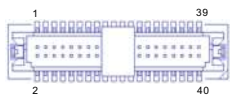
2 : Panel Power Select (PNL_PWR1)
1-2 : +3V
2-3 : +5V
4-5 : +12V



3 : BL2
Open: Protect LCD_VCC
Short: No Protect LCD_VCC



4 : LVDS Panel Connector



PIN	Signal Name	PIN	Signal Name
1	R_LVDD	19	LVDS_A_CLK
2	LDDC_CLK	20	LVDS_B_CLK
3	LVDS_A_DATA#	21	LVDS_B_DATA#
4	LVDS_A_DATA0	22	LVDS_B_DATA0
5	LVDS_A_DATA1	23	LVDS_B_DATA1
6	LVDS_A_DATA2	24	LVDS_B_DATA2
7	LVDS_A_DATA3	25	LVDS_B_DATA3
8	LVDS_A_DATA4	26	DPLVDD_EN
9	LVDS_A_DATA5	27	LVDS_B_DATA3
10	LVDS_A_DATA6	28	LVDS_B_CLK
11	LVDS_A_DATA7	29	LVDS_B_CLK#
12	LVDS_A_DATA8	30	CON_LBKLT_EN_R
13	LVDS_A_DATA9	31	CON_LBKLT_CTL_R
14	LVDS_A_DATA10	32	CON_LBKLT_VCC
15	LVDS_A_DATA11	33	LVDS_B_DATA3
16	LVDS_A_DATA12	34	LVDS_B_DATA3
17	LVDS_A_DATA13	35	LVDS_B_DATA3
18	LVDS_A_DATA14	36	LVDS_B_DATA3
19	LVDS_A_DATA15	37	LVDS_B_DATA3
20	LVDS_A_DATA16	38	LVDS_B_DATA3
21	LVDS_A_DATA17	39	LVDS_B_DATA3
22	LVDS_A_DATA18	40	LVDS_B_DATA3
23	LVDS_A_DATA19		
24	LVDS_A_DATA20		
25	LVDS_A_DATA21		
26	LVDS_A_DATA22		
27	LVDS_A_DATA23		
28	LVDS_A_DATA24		
29	LVDS_A_DATA25		
30	LVDS_A_DATA26		
31	LVDS_A_DATA27		
32	LVDS_A_DATA28		
33	LVDS_A_DATA29		
34	LVDS_A_DATA30		
35	LVDS_A_DATA31		
36	LVDS_A_DATA32		
37	LVDS_A_DATA33		
38	LVDS_A_DATA34		
39	LVDS_A_DATA35		
40	LVDS_A_DATA36		

5 : Backlight & Amp Volume Control (BLT_VOL1)



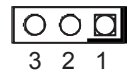
PIN	Signal Name
7	GND
6	GND
5	GPIO_BLT_DW
4	GPIO_BLT_UP
3	PWRDN
2	GPIO_VOL_DW
1	GPIO_VOL_UP

6 : Inverter Power Control Wafer (BLT_PWR1)



PIN	Signal Name
1	GND
2	GND
3	CON_LBKLT_CTL
4	CON_LBKLT_EN
5	LCD_BLT_VCC
6	LCD_BLT_VCC

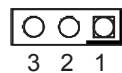
7 : Backlight Control Level (BLT_PWM1) (CON_LBKLT_CTL)
1-2 : +3V (eDP)
2-3 : +5V (LVDS)



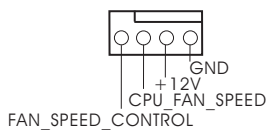
8 : BL1
Open: Protect LCD_BLT_VCC
Short: No Protect LCD_BLT_VCC



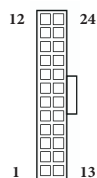
9 : Backlight Power Select (LCD_BLT_VCC) (BKT_PWR1)
1-2 : LCD_BLT_VCC : +5V
2-3 : LCD_BLT_VCC : +12V



10 : CPU FAN Connector (+12V)



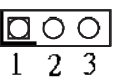
11 : 24-pin ATX Power Input Connector



12 : PWR LOSS Header (PWR_LOSS1)
Short : Power Loss
Close : no Power Loss



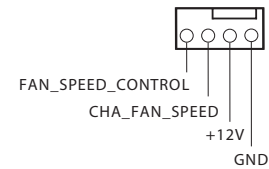
USB Power Setting Jumpers
25 : USB2_PWR1 (For USB2_4_5)
USB2_PWR2 (For USB2_6_7)



32 : USB3_PWR1 (For USB3_1_2)
USB3_PWR2 (For USB3_3_4)

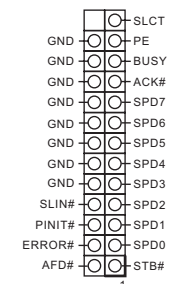


13 : Chassis FAN Connector (+12V)



14 : Printer Port / GPIO Header (LPT_GPIO1)

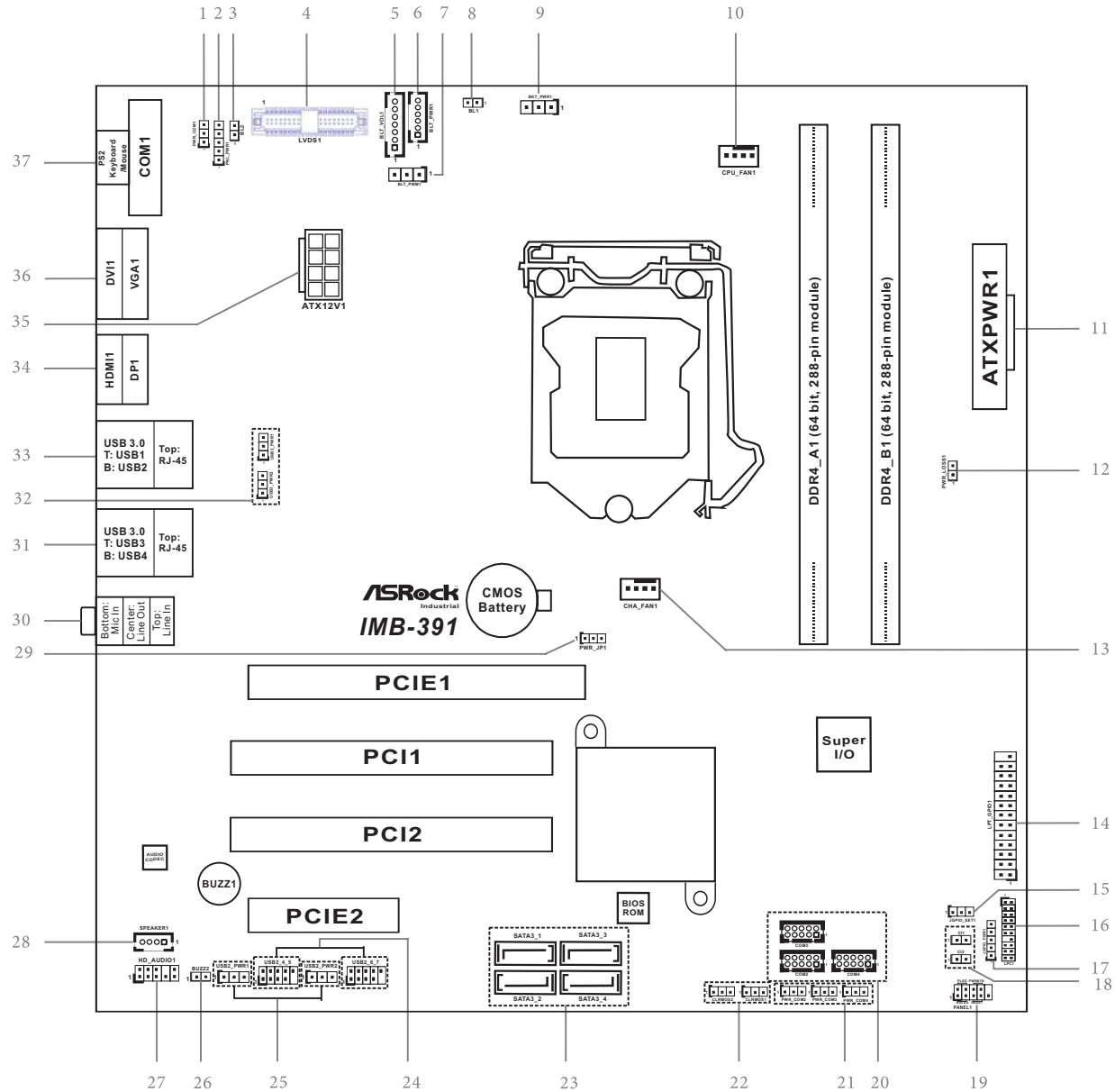
Printer Port:



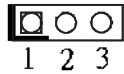
GPIO:

PIN	Signal Name	PIN	Signal Name
26	NC	25	NA
24	GND	23	SIO_GP30
22	GND	21	SIO_GP31
20	GND	19	SIO_GP32
18	GND	17	SIO_GP33
16	GND	15	SIO_GP34
14	GND	13	SIO_GP35
12	JGPIOPWR	11	SIO_GP36
10	JGPIOPWR	9	SIO_GP37
8	SIO_GP43	7	SIO_GP40
6	SIO_GP44	5	SIO_GP41
4	SIO_GP45	3	SIO_GP42
2	SIO_GP46	1	SIO_GP47

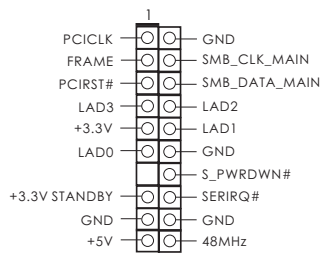
* If you want to use the printer port function, please short pin4 and pin5 on Digital Input / Output Power Select (JGPIO_PWR1).



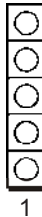
15 : Digital Input / Output Power Select (JGPIO_SET1)
 1-2 : +5V
 2-3 : GND



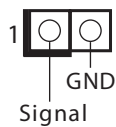
16 : LPC Header



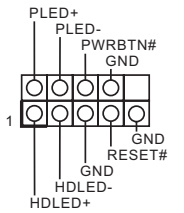
17 : Digital Input / Output Power Select (JGPIO_PWR) (JGPIO_PWR1)
 1-2 : +12V
 2-3 : +5V
 3-4 : +5V
 4-5 : GND



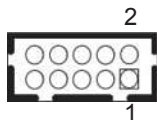
18 : Chassis Intrusion Headers (CI1, CI2)
 Short : Active Case Open
 Open : Normal



19 : System Panel Header

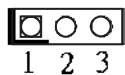


20 : COM Port Headers (COM2, 3, 4) (RS232)

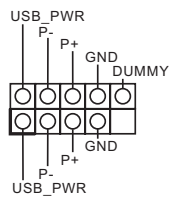


PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
10	N/A	8	CCTS#	6	DDSR#	4	DDTR#	2	RRXD
9	+5V	7	RRTS#	5	GND	3	TTXD	1	DDCD#

22 : Clear CMOS Headers
 CLRMOS1 :
 1-2 : Normal
 2-3 : Clear CMOS
 CLRMOS2 :
 1-2 : +BATT
 2-3 : +3VSB_A



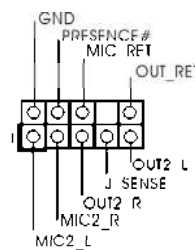
23 : SATA3 Connectors (SATA3_1 ~ SATA3_4)
 24 : USB 2.0 Headers (USB2_4_5, USB2_6_7)



26 : Buzzer (BUZZ2)



27 : Front Panel Audio Header

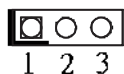


28 : 3W Audio AMP Output Wafer



PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
4	OUTRN	3	OUTRP	2	OUTLP	1	OUTLN

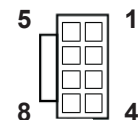
29 : ATX/AT Mode Jumper (PWR_JP1)
 1-2 : AT Mode
 2-3 : ATX Mode



30 : Audio Jacks
 Blue - Line In
 Green - Line Out
 Pink - Mic In

31 : Top : RJ45 LAN Port (LAN2)
 Bottom : USB 3.0 Ports (USB3_3_4)
 33 : Top : RJ45 LAN Port (LAN1)
 Bottom : USB 3.0 Ports (USB3_1_2)
 34 : Top : DisplayPort (DP1)
 Bottom : HDMI Port (HDMI1)

35 : ATX 12V Power Connector
 36 : Top : D-Sub Port (VGA1)
 Bottom : DVI Port (DVI1)



37 : Top : COM Port (COM1) (RS232/422/485)*
 Bottom : PS/2 Keyboard/Mouse Port

* This motherboard supports RS232/422/485 on COM1 port. Please refer to below table for the pin definition. In addition, COM1 port (RS232/422/485) can be adjusted in BIOS setup utility > Advanced Screen > Super IO Configuration. You may refer to our user manual for details.

COM1 Port Pin Definition

PIN	RS232	RS422	RS485
1	DCD, Data Carrier Detect	TX-	RTX-
2	RXD, Receive Data	RX+	N/A
3	TXD, Transmit Data	TX+	RTX+
4	DTR, Data Terminal Ready	RX-	N/A
5	GND	GND	GND
6	DSR, Data Set Ready	N/A	N/A
7	RTS, Request To Send	N/A	N/A
8	CTS, Clear To Send	N/A	N/A
9	No Power/5V/12V	N/A	N/A