

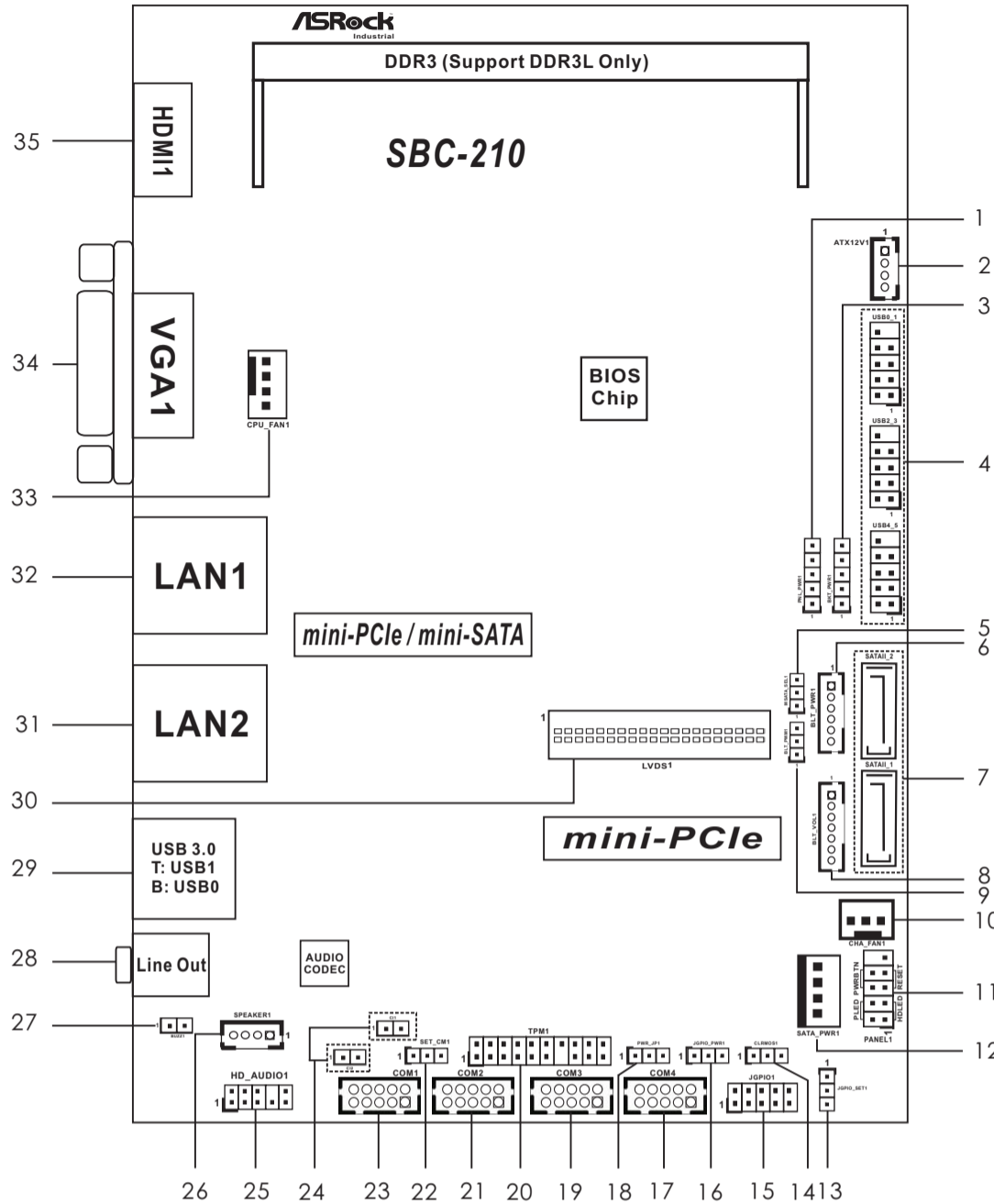


P/N: 15G06M014002AK V1.1b

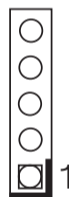
ASRock® Jumpers and headers setting guide

SBC-210

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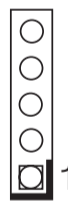
1 : Panel Power Selection (LCD_VCC) (PNL_PWR1)
 1-2 : +3V
 2-3 : +5V
 3-4 : +5V
 4-5 : +12V



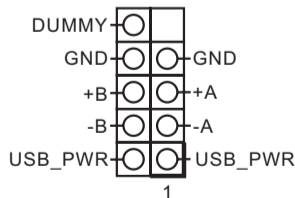
2 : ATX Power Connector (Input 9V-36V)
 1-4 : GND
 2-3 : DC Input



3 : Backlight Power Selection (LCD_BLT_VCC) (BKT_PWR1)
 1-2 : +5V
 2-3 : +12V
 3-4 : +12V
 4-5 : DC_IN Power



4 : USB2.0 Headers (USB0_1, USB2_3, USB4_5)



5 : mSATA Selection
 1-2 : SATAII_2 + mini-PCle
 2-3 : mSATA, SATAII_2 no function



6 : Inverter Power Control Wafer (BLT_PWR1)

PIN	Signal Name
1	GND
2	GND
3	CON_LBKLT_CTL
4	CON_LBKLT_EN
5	LCD_BLT_VCC
6	LCD_BLT_VCC



7 : SATA2 Connectors (SATAII_1, SATAII_2)



8 : Backlight & Amp Volume Control (BLT_VOL1)

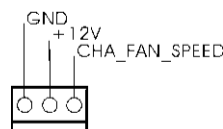
PIN	Signal Name
1	GPIO_VOL_UP
2	GPIO_VOL_DW
3	PWRDN
4	GPIO_BLT_UP
5	GPIO_BLT_DW
6	GND
7	GND



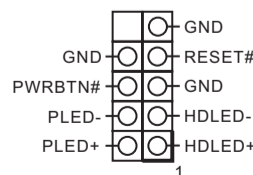
9 : Backlight Control Level (BLT_PWM1)
 1-2 : +3V
 2-3 : +5V



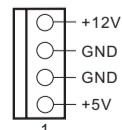
10 : 3-Pin Chassis FAN Connector (+12V)



11 : System Panel Header



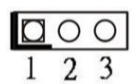
12 : SATA Power Output Connector



13 : GPIO Default Setting
 1-2 : Pull-High
 2-3 : Pull-Low

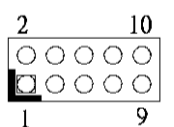


14 : Clear CMOS Header
 1-2 : Normal
 2-3 : Clear CMOS

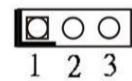


15 : Digital Input / Output Pin Header

PIN	Signal Name	PIN	Signal Name
1	SIO_GP24	2	SIO_GP20
3	SIO_GP25	4	SIO_GP21
5	SIO_GP26	6	SIO_GP22
7	SIO_GP27	8	SIO_GP23
9	JGPIO_PWR	10	GND

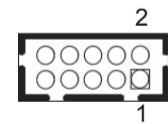


16 : Digital Input / Output Power select
 1-2 : +12V
 2-3 : +5V



COM1, 2, 3, 4 Headers

17 : COM4
 19 : COM3
 21 : COM2
 23 : COM1

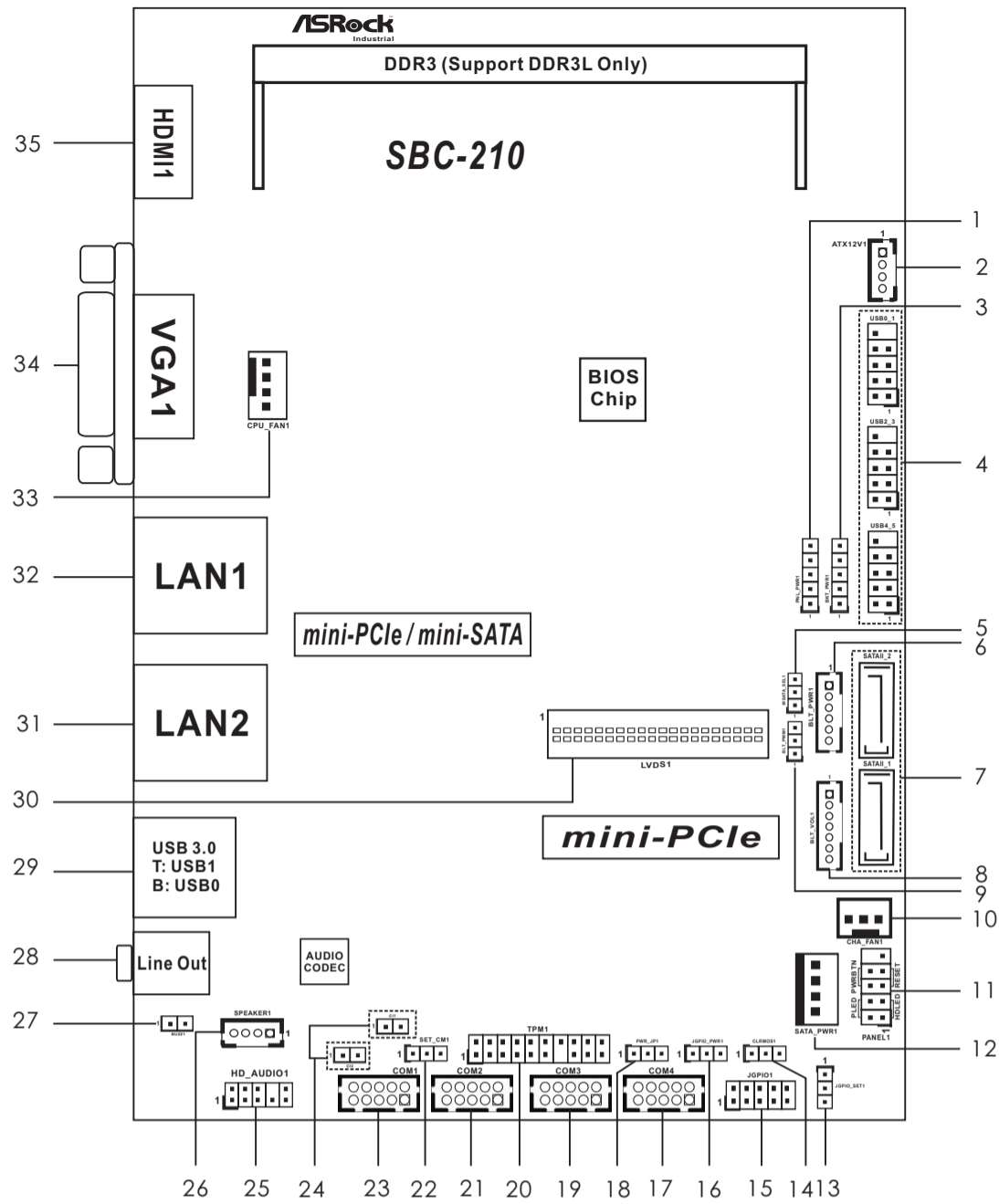


PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
10	DUMMY	8	CCTS#	6	DDSR#	4	DDTR#	2	RRXD#
9	DUMMY	7	RRTS#	5	GND	3	TTXD	1	DDCD#

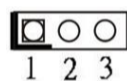
* This motherboard supports RS232/422/485 on COM1 port. Please refer to below table for the pin definition. In addition, COM1 port (RS232/422/485) can be adjusted in BIOS setup utility > Advanced Screen > Super IO Configuration. You may refer to page 29 of our user manual for details.

COM1 Port Pin Definition

PIN	RS232	RS422	RS485
1	DCD	TX-	RTX-
2	RXD	RX+	N/A
3	TXD	TX+	RTX+
4	DTR	RX-	N/A
5	GND	GND	GND
6	DSR	N/A	N/A
7	RTS	N/A	N/A
8	CTS	N/A	N/A
9	NA/+5V/+12V	N/A	N/A

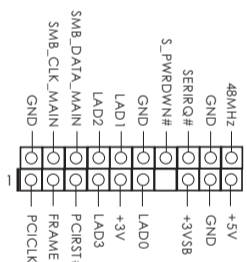


18 : ATX/AT Mode Selection
 1-2 : AT mode
 2-3 : ATX mode

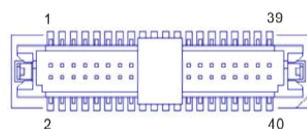


28 : Audio Jack (Green - Line Out)
 29 : USB3.0 Ports (USB3_01)

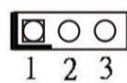
20 : TPM Header



30 : LVDS Panel Connector



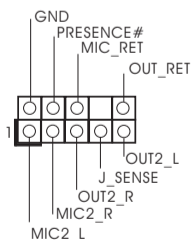
22 : COM1 Pin9 PWR Setting
 1-2 : +5V
 2-3 : +12V



24 : Chassis Intrusion Headers (CI1, CI2)



25 : Front Panel Audio Header

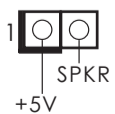


26 : 3W Audio AMP Output Wafer

PIN	Signal Name
1	SPK L-
2	SPK L+
3	SPK R+
4	SPK R-

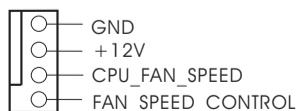


27 : 2-Pin Buzzer Header



31 : RJ45 LAN Port 2
 32 : RJ45 LAN Port 1

33 : 4-Pin CPU FAN Connector (+12V)



34 : VGA Port
 35 : HDMI Port

PIN	Signal Name	PIN	Signal Name
1	LCD_VCC	2	LCD_VCC
3	+3V	4	N/A
5	N/A	6	LVDS_A_DATA0#
7	LVDS_A_DATA0	8	GND1
9	LVDS_A_DATA1#	10	LVDS_A_DATA1
11	GND6	12	LVDS_A_DATA2#
13	LVDS_A_DATA2	14	GND2
15	LVDS_A_DATA3#	16	LVDS_A_DATA3
17	GND7	18	LVDS_A_CLK#
19	LVDS_A_CLK	20	GND3
21	LVDS_B_DATA0#	22	LVDS_B_DATA0
23	GND8	24	LVDS_B_DATA1#
25	LVDS_B_DATA1	26	GND4
27	LVDS_B_DATA2#	28	LVDS_B_DATA2
29	DPLVDD_EN	30	LVDS_B_DATA3#
31	LVDS_B_DATA3	32	GND5
33	LVDS_B_CLK#	34	LVDS_B_CLK
35	GND9	36	CON_LBKLT_EN
37	CON_LBKLT_CTR	38	LCD_BLT_VCC
39	LCD_BLT_VCC	40	LCD_BLT_VCC