

* This motherboard supports RS232/422/485 on COM1/COM2 port. Please refer to below table for the pin definition. In addition, COM1/COM2 port (RS232/422/485) can be adjusted in BIOS setup utility > Advanced Screen > Super IO Configuration. You may refer to page 32 of our user manual for details.

COM1/COM2 Port Pin Definition

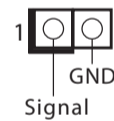
PIN	RS232	RS422	RS485
1	DCD	TX-	RTX-
2	RXD	RX+	N/A
3	TXD	TX+	RTX+
4	DTR	RX-	N/A
5	GND	GND	GND
6	DSR	N/A	N/A
7	RTS	N/A	N/A
8	CTS	N/A	N/A
9	NA/+5V/+12V	N/A	N/A

28 : 3W Audio AMP Output Wafer

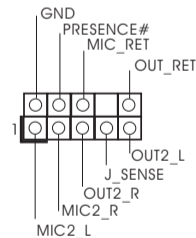
PIN	Signal Name
1	SPK L-
2	SPK L+
3	SPK R+
4	SPK R-

- 34 : HDMI Port (HDMI2)
- 35 : HDMI Port (HDMI1)
- 36 : USB3.0 Ports (USB23)
- 37 : USB3.0 Ports (USB01)

29 : Chassis Intrusion Headers (CI1, CI2)



30 : Front Panel Audio Header



COM Port Pin9 PWR Setting Jumpers

- 18 : PWR_COM4 (For COM Port4)
- 20 : PWR_COM3 (For COM Port3)
- 22 : PWR_COM2 (For COM Port2)
- 24 : PWR_COM1 (For COM Port1)



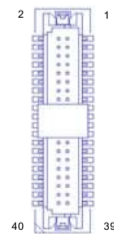
- 31 : RJ45 LAN Port (LAN2)
- 32 : RJ45 LAN Port (LAN1)

25 : GPIO Default Setting



33 : LVDS Panel Connector

PIN	Signal Name	PIN	Signal Name
2	LCD_VCC	1	LCD_VCC
4	LDDC_CLK	3	+3.3V
6	LVDS_A_DATA0#	5	LDDC_DATA
8	GND	7	LVDS_A_DATA0
10	LVDS_A_DATA1	9	LVDS_A_DATA1#
12	LVDS_A_DATA2#	11	GND
14	GND	13	LVDS_A_DATA2
16	LVDS_A_DATA3	15	LVDS_A_DATA3#
18	LVDS_A_CLK#	17	GND
20	GND	19	LVDS_A_CLK
22	LVDS_B_DATA0	21	LVDS_B_DATA0#
24	LVDS_B_DATA1#	23	GND
26	GND	25	LVDS_B_DATA1
28	LVDS_B_DATA2	27	LVDS_B_DATA2#
30	LVDS_B_DATA3#	29	DPLVDD_EN
32	GND	31	LVDS_B_DATA3
34	LVDS_B_CLK	33	LVDS_B_CLK#
36	CON_LBKL_T_EN	35	GND
38	LCD_BLT_VCC	37	CON_LBKL_CTL
40	LCD_BLT_VCC	39	LCD_BLT_VCC



26 : Digital Input / Output Power Select



27 : Digital Input / Output Pin Header

PIN	Signal Name	PIN	Signal Name
1	SIO_GP34	2	SIO_GP30
3	SIO_GP35	4	SIO_GP31
5	SIO_GP36	6	SIO_GP32
7	SIO_GP37	8	SIO_GP33
9	JGPIO_PWR	10	GND

