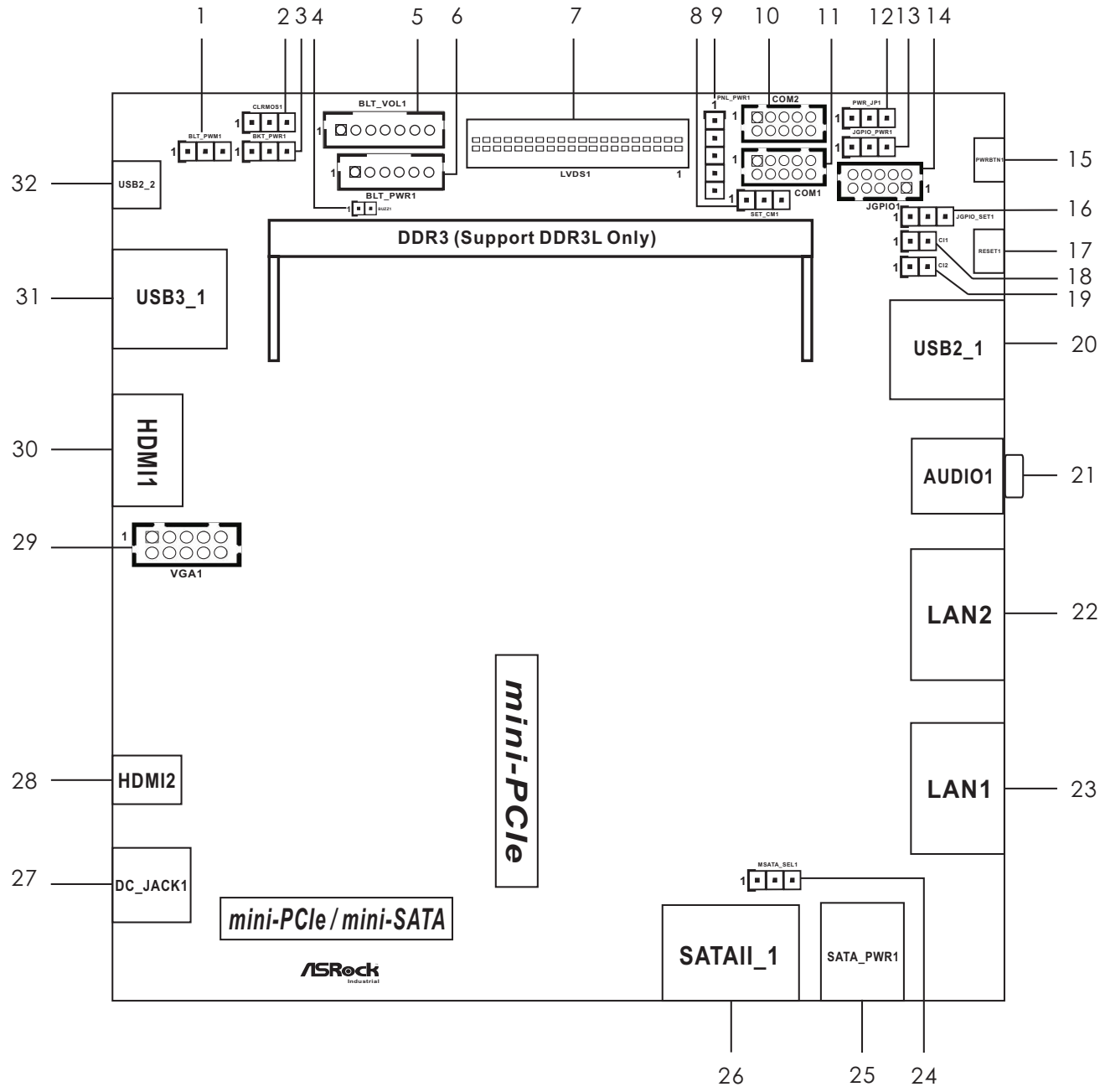


# ASRock® Jumpers and headers setting guide

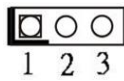
UTX-110

UTX-111

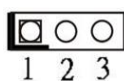
The terms HDMI™ and HDMI High-Definition Multimedia Interface, and the HDMI logo are trademarks or registered trademarks of HDMI Licensing LLC in the United States and other countries.



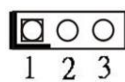
1 : Backlight Control Level (BLT\_PWM1)  
1-2 : +3V  
2-3 : +5V



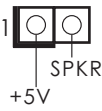
2 : Clear CMOS Header  
1-2 : Normal  
2-3 : Clear CMOS



3 : Backlight Power Select (LCD\_BLT\_VCC) (BKT\_PWR1)  
1-2 : +5V  
2-3 : +12V



4 : 2-Pin Buzzer Header  
+5V  
SPKR



5 : Backlight & Amp Volume Control (BLT\_VOL1)

PIN	Signal Name
1	GPIO_VOL_UP
2	GPIO_VOL_DW
3	PWRDN
4	GPIO_BLT_UP
5	GPIO_BLT_DW
6	GND
7	GND



6 : Inverter Power Control Wafer (BLT\_PWR1)

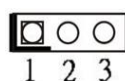
PIN	Signal Name
1	GND
2	GND
3	CON_LBKLT_CTL
4	CON_LBKLT_EN
5	LCD_BLT_VCC
6	LCD_BLT_VCC



7 : LVDS Panel Connector

PIN	Signal Name	PIN	Signal Name
1	LCD_VCC	2	LCD_VCC
3	+3V	4	N/A
5	N/A	6	LVDS_A_DATA0#
7	LVDS_A_DATA0	8	GND1
9	LVDS_A_DATA1#	10	LVDS_A_DATA1
11	GND6	12	LVDS_A_DATA2#
13	LVDS_A_DATA2	14	GND2
15	LVDS_A_DATA3#	16	LVDS_A_DATA3
17	GND7	18	LVDS_A_CLK#
19	LVDS_A_CLK	20	GND3
21	LVDS_B_DATA0#	22	LVDS_B_DATA0
23	GND8	24	LVDS_B_DATA1#
25	LVDS_B_DATA1	26	GND4
27	LVDS_B_DATA2#	28	LVDS_B_DATA2
29	DPLVDD_EN	30	LVDS_B_DATA3#
31	LVDS_B_DATA3	32	GND5
33	LVDS_B_CLK#	34	LVDS_B_CLK
35	GND9	36	CON_LBKLT_EN
37	CON_LBKLT_CTR	38	LCD_BLT_VCC
39	LCD_BLT_VCC	40	LCD_BLT_VCC

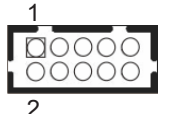
8 : COM1 Pin9 PWR Setting  
1-2 : +5V  
2-3 : +12V



9 : Panel Power Select (LCD\_VCC) (PNL\_PWR1)  
1-2 : +3V  
2-3 : +5V  
3-4 : +5V  
4-5 : +12V



COM1, 2 Headers  
10 : COM2  
11 : COM1



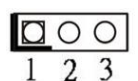
PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
1	DDCD#	3	TTXD	5	GND	7	RRTS#	9	DUMMY
2	RRXD	4	DDTR#	6	DDSR#	8	CCTS#	10	DUMMY

\* This motherboard supports RS232/422/485 on COM1 port. Please refer to below table for the pin definition. In addition, COM1 port (RS232/422/485) can be adjusted in BIOS setup utility > Advanced Screen > Super IO Configuration. You may refer to page 25 of our user manual for details.

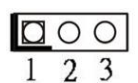
### COM1 Port Pin Definition

PIN	RS232	RS422	RS485
1	DCD	TX-	RTX-
2	RXD	RX+	N/A
3	TXD	TX+	RTX+
4	DTR	RX-	N/A
5	GND	GND	GND
6	DSR	N/A	N/A
7	RTS	N/A	N/A
8	CTS	N/A	N/A
9	+5V/+12V	N/A	N/A

12 : ATX/AT Mode Select  
1-2 : AT Mode  
2-3 : ATX Mode



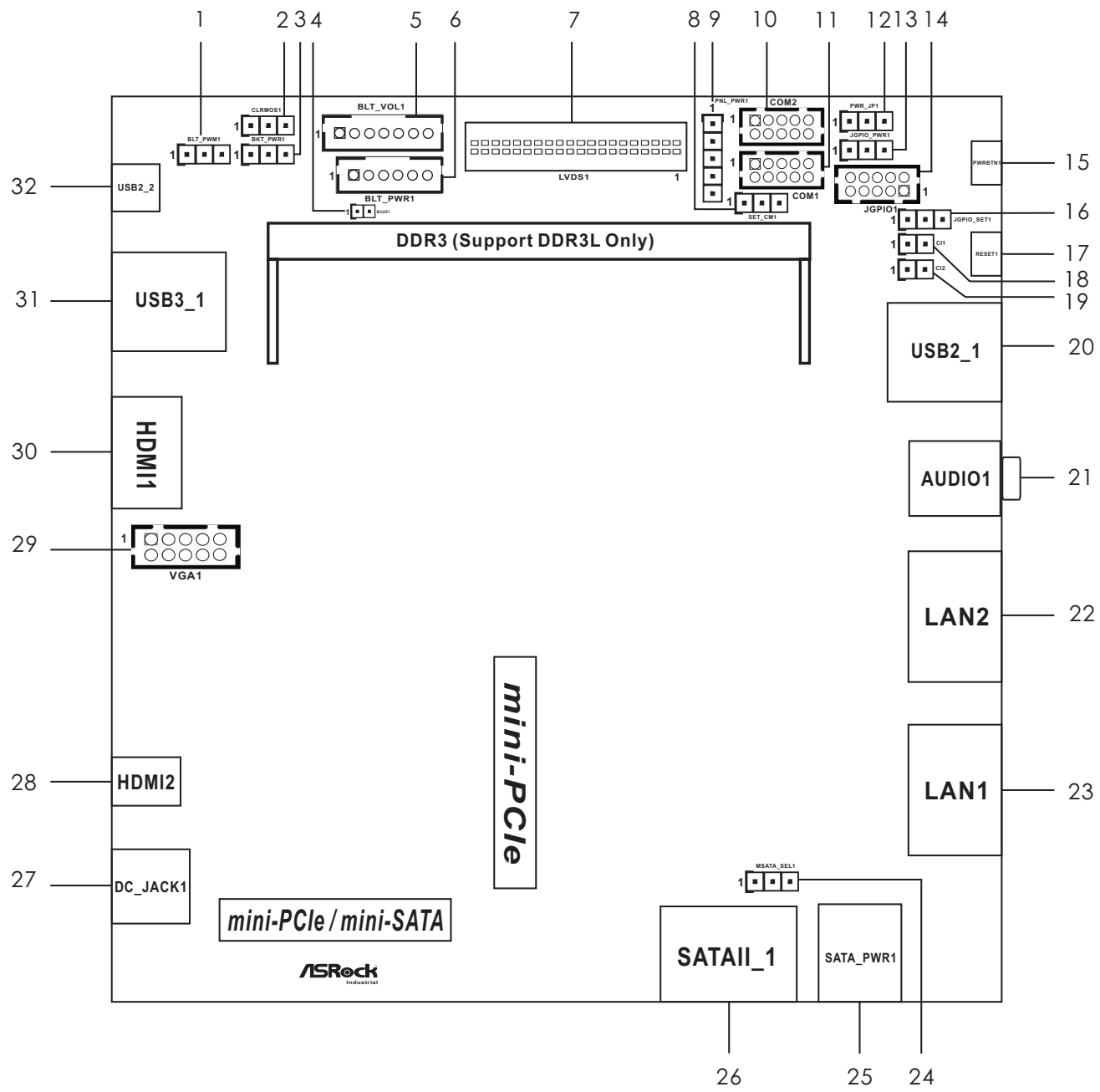
13 : Digital Input / Output Power Select  
1-2 : +12V  
2-3 : +5V



14 : Digital Input / Output Pin Header

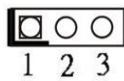


PIN	Signal Name	PIN	Signal Name
1	JGPIO_PWR	2	GND
3	SIO_GP27	4	SIO_GP23
5	SIO_GP26	6	SIO_GP22
7	SIO_GP25	8	SIO_GP21
9	SIO_GP24	10	SIO_GP20



15 : Power Button

16 : GPIO Default Setting  
 1-2 : Pull-High  
 2-3 : Pull-Low



30 : HDMI Port (HDMI1)

31 : USB3.0 Port (USB3\_1)

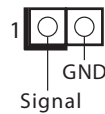
32 : Micro USB2.0 Port (USB2\_2)

17 : Reset Button

Chassis Intrusion Headers

18 : CI1

Close : Active Case Open  
 Open : Normal

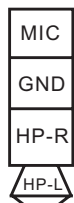


19 : CI2

Close : Normal  
 Open : Active Case Open

20 : USB2.0 Port (USB2\_1)

21 : 3.5mm Audio Jack (CTIA Standard)

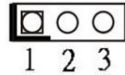


22 : RJ45 LAN Port 2

23 : RJ45 LAN Port 1

24 : mSATA Select

1-2 : mini-PCle  
 2-3 : mSATA



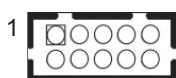
25 : SATA Power Output Connector

26 : SATA2 Connector (SATAII\_1)

27 : DC Jack Port (+12V Only)

28 : Micro HDMI Port (HDMI2)

29 : VGA Connector



PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name	PIN	Signal Name
1	DDC_CLK	3	HSYNC	5	BLUE	7	GREEN	9	RED
2	DDC_DATA	4	VSYSNC	6	GND	8	GND	10	GND